
Gallium Documentation

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VMware, X.org, Nouveau

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Contents:

Introduction

1.1 What is Gallium?

Gallium is essentially an API for writing graphics drivers in a largely device-agnostic fashion. It provides several objects which encapsulate the core services of graphics hardware in a straightforward manner.

Debugging

Debugging utilities in gallium.

2.1 Debug Variables

All drivers respond to a set of common debug environment variables, as well as some driver-specific variables. Set them as normal environment variables for the platform or operating system you are running. For example, for Linux this can be done by typing “export var=value” into a console and then running the program from that console.

2.1.1 Common

GALLIUM_PRINT_OPTIONS Type: bool **Default: false**

This option controls if the debug variables should be printed to stderr. This is probably the most useful variable, since it allows you to find which variables a driver uses.

GALLIUM_GALAHAD Type: bool **Default: false**

Controls if the *Galahad* sanity checker module should be used.

GALLIUM_RBUG Type: bool **Default: false**

Controls if the *Remote Debugger* should be used.

GALLIUM_TRACE Type: string **Default: “”**

If set, this variable will cause the Trace output to be written to the specified file. Paths may be relative or absolute; relative paths are relative to the working directory. For example, setting it to “trace.xml” will cause the trace to be written to a file of the same name in the working directory.

GALLIUM_DUMP_CPU Type: bool **Default: false**

Dump information about the current CPU that the driver is running on.

TGSI_PRINT_SANITY Type: bool **Default: false**

Gallium has a built-in shader sanity checker. This option controls whether the shader sanity checker prints its warnings and errors to stderr.

DRAW_USE_LLVM Type: bool **Default: false**

Whether the *Draw* module will attempt to use LLVM for vertex and geometry shaders.

2.1.2 State tracker-specific

ST_DEBUG Type: flags **Default: 0x0**

Debug *Flags* for the GL state tracker.

2.1.3 Driver-specific

I915_DEBUG Type: flags **Default: 0x0**

Debug *Flags* for the i915 driver.

I915_NO_HW Type: bool **Default: false**

Stop the i915 driver from submitting commands to the hardware.

I915_DUMP_CMD Type: bool **Default: false**

Dump all commands going to the hardware.

LP_DEBUG Type: flags **Default: 0x0**

Debug *Flags* for the llvmpipe driver.

LP_NUM_THREADS Type: int **Default: number of CPUs**

Number of threads that the llvmpipe driver should use.

2.1.4 Flags

The variables of type “flags” all take a string with comma-separated flags to enable different debugging for different parts of the drivers or state tracker. If set to “help”, the driver will print a list of flags which the variable accepts. Order does not matter.

2.2 Remote Debugger

The remote debugger, commonly known as rbug, allows for runtime inspections of *Context*, *Screen*, Resource and *Shader* objects; and pausing and stepping of *Draw* calls. Is used with rbug-gui which is hosted outside of the main mesa repository. rbug is can be used over a network connection, so the debugger does not need to be on the same machine.

TGSIG, Tungsten Graphics Shader Infrastructure, is an intermediate language for describing shaders. Since Gallium is inherently shaderful, shaders are an important part of the API. TGSIG is the only intermediate representation used by all drivers.

3.1 Basics

All TGSIG instructions, known as *opcodes*, operate on arbitrary-precision floating-point four-component vectors. An opcode may have up to one destination register, known as *dst*, and between zero and three source registers, called *src0* through *src2*, or simply *src* if there is only one.

Some instructions, like *I2F*, permit re-interpretation of vector components as integers. Other instructions permit using registers as two-component vectors with double precision; see Double Opcodes.

When an instruction has a scalar result, the result is usually copied into each of the components of *dst*. When this happens, the result is said to be *replicated* to *dst*. *RCP* is one such instruction.

3.2 Instruction Set

3.2.1 Core ISA

These opcodes are guaranteed to be available regardless of the driver being used.

ARL (Address Register Load)

$$dst.x = [src.x]$$

$$dst.y = [src.y]$$

$$dst.z = [src.z]$$

$$dst.w = [src.w]$$

MOV (Move)

$$\begin{aligned}dst.x &= src.x \\dst.y &= src.y \\dst.z &= src.z \\dst.w &= src.w\end{aligned}$$

LIT (Light Coefficients)

$$\begin{aligned}dst.x &= 1 \\dst.y &= \max(src.x, 0) \\dst.z &= (src.x > 0) ? \max(src.y, 0)^{\text{clamp}(src.w, -128, 128)} : 0 \\dst.w &= 1\end{aligned}$$

RCP (Reciprocal)

This instruction replicates its result.

$$dst = \frac{1}{src.x}$$

RSQ (Reciprocal Square Root)

This instruction replicates its result.

$$dst = \frac{1}{\sqrt{|src.x|}}$$

EXP (Approximate Exponential Base 2)

$$\begin{aligned}dst.x &= 2^{\lfloor src.x \rfloor} \\dst.y &= src.x - \lfloor src.x \rfloor \\dst.z &= 2^{src.x} \\dst.w &= 1\end{aligned}$$

LOG (Approximate Logarithm Base 2)

$$\begin{aligned}dst.x &= \lfloor \log_2 |src.x| \rfloor \\dst.y &= \frac{|src.x|}{2^{\lfloor \log_2 |src.x| \rfloor}} \\dst.z &= \log_2 |src.x| \\dst.w &= 1\end{aligned}$$

MUL (Multiply)

$$\begin{aligned}dst.x &= src0.x \times src1.x \\dst.y &= src0.y \times src1.y \\dst.z &= src0.z \times src1.z \\dst.w &= src0.w \times src1.w\end{aligned}$$

ADD (Add)

$$\begin{aligned}dst.x &= src0.x + src1.x \\dst.y &= src0.y + src1.y \\dst.z &= src0.z + src1.z \\dst.w &= src0.w + src1.w\end{aligned}$$

DP3 (3-component Dot Product)

This instruction replicates its result.

$$dst = src0.x \times src1.x + src0.y \times src1.y + src0.z \times src1.z$$

DP4 (4-component Dot Product)

This instruction replicates its result.

$$dst = src0.x \times src1.x + src0.y \times src1.y + src0.z \times src1.z + src0.w \times src1.w$$

DST (Distance Vector)

$$\begin{aligned}dst.x &= 1 \\dst.y &= src0.y \times src1.y \\dst.z &= src0.z \\dst.w &= src1.w\end{aligned}$$

MIN (Minimum)

$$\begin{aligned}dst.x &= \min(src0.x, src1.x) \\dst.y &= \min(src0.y, src1.y) \\dst.z &= \min(src0.z, src1.z) \\dst.w &= \min(src0.w, src1.w)\end{aligned}$$

MAX (Maximum)

$$\begin{aligned}dst.x &= \max(src0.x, src1.x) \\dst.y &= \max(src0.y, src1.y) \\dst.z &= \max(src0.z, src1.z) \\dst.w &= \max(src0.w, src1.w)\end{aligned}$$

SLT (Set On Less Than)

$$\begin{aligned}dst.x &= (src0.x < src1.x)?1 : 0 \\dst.y &= (src0.y < src1.y)?1 : 0 \\dst.z &= (src0.z < src1.z)?1 : 0 \\dst.w &= (src0.w < src1.w)?1 : 0\end{aligned}$$

SGE (Set On Greater Equal Than)

$$\begin{aligned}dst.x &= (src0.x \geq src1.x)?1 : 0 \\dst.y &= (src0.y \geq src1.y)?1 : 0 \\dst.z &= (src0.z \geq src1.z)?1 : 0 \\dst.w &= (src0.w \geq src1.w)?1 : 0\end{aligned}$$

MAD (Multiply And Add)

$$\begin{aligned}dst.x &= src0.x \times src1.x + src2.x \\dst.y &= src0.y \times src1.y + src2.y \\dst.z &= src0.z \times src1.z + src2.z \\dst.w &= src0.w \times src1.w + src2.w\end{aligned}$$

SUB (Subtract)

$$\begin{aligned}dst.x &= src0.x - src1.x \\dst.y &= src0.y - src1.y \\dst.z &= src0.z - src1.z \\dst.w &= src0.w - src1.w\end{aligned}$$

LRP (Linear Interpolate)

$$\begin{aligned}dst.x &= src0.x \times src1.x + (1 - src0.x) \times src2.x \\dst.y &= src0.y \times src1.y + (1 - src0.y) \times src2.y \\dst.z &= src0.z \times src1.z + (1 - src0.z) \times src2.z \\dst.w &= src0.w \times src1.w + (1 - src0.w) \times src2.w\end{aligned}$$

CND (Condition)

$$\begin{aligned}dst.x &= (src2.x > 0.5)?src0.x : src1.x \\dst.y &= (src2.y > 0.5)?src0.y : src1.y \\dst.z &= (src2.z > 0.5)?src0.z : src1.z \\dst.w &= (src2.w > 0.5)?src0.w : src1.w\end{aligned}$$

DP2A (2-component Dot Product And Add)

$$\begin{aligned}dst.x &= src0.x \times src1.x + src0.y \times src1.y + src2.x \\dst.y &= src0.x \times src1.x + src0.y \times src1.y + src2.x \\dst.z &= src0.x \times src1.x + src0.y \times src1.y + src2.x \\dst.w &= src0.x \times src1.x + src0.y \times src1.y + src2.x\end{aligned}$$

FRC (Fraction)

$$\begin{aligned}dst.x &= src.x - \lfloor src.x \rfloor \\dst.y &= src.y - \lfloor src.y \rfloor \\dst.z &= src.z - \lfloor src.z \rfloor \\dst.w &= src.w - \lfloor src.w \rfloor\end{aligned}$$

CLAMP (Clamp)

$$\begin{aligned}dst.x &= clamp(src0.x, src1.x, src2.x) \\dst.y &= clamp(src0.y, src1.y, src2.y) \\dst.z &= clamp(src0.z, src1.z, src2.z) \\dst.w &= clamp(src0.w, src1.w, src2.w)\end{aligned}$$

FLR (Floor)

This is identical to [ARL](#).

$$\begin{aligned}dst.x &= \lfloor src.x \rfloor \\dst.y &= \lfloor src.y \rfloor \\dst.z &= \lfloor src.z \rfloor \\dst.w &= \lfloor src.w \rfloor\end{aligned}$$

ROUND (Round)

$$\begin{aligned}dst.x &= round(src.x) \\dst.y &= round(src.y) \\dst.z &= round(src.z) \\dst.w &= round(src.w)\end{aligned}$$

EX2 (Exponential Base 2)

This instruction replicates its result.

$$dst = 2^{src.x}$$

LG2 (Logarithm Base 2)

This instruction replicates its result.

$$dst = \log_2 src.x$$

POW (Power)

This instruction replicates its result.

$$dst = src0.x^{src1.x}$$

XPD (Cross Product)

$$\begin{aligned}dst.x &= src0.y \times src1.z - src1.y \times src0.z \\dst.y &= src0.z \times src1.x - src1.z \times src0.x \\dst.z &= src0.x \times src1.y - src1.x \times src0.y \\dst.w &= 1\end{aligned}$$

ABS (Absolute)

$$\begin{aligned}dst.x &= |src.x| \\dst.y &= |src.y| \\dst.z &= |src.z| \\dst.w &= |src.w|\end{aligned}$$

RCC (Reciprocal Clamped)

This instruction replicates its result.

XXX cleanup on aisle three

$$dst = (1/src.x) > 0 ? clamp(1/src.x, 5.42101e - 020, 1.884467e + 019) : clamp(1/src.x, -1.884467e + 019, -5.42101e - 020)$$

DPH (Homogeneous Dot Product)

This instruction replicates its result.

$$dst = src0.x \times src1.x + src0.y \times src1.y + src0.z \times src1.z + src1.w$$

COS (Cosine)

This instruction replicates its result.

$$dst = \cos src.x$$

DDX (Derivative Relative To X)

$$\begin{aligned}dst.x &= \text{partial}x(src.x) \\dst.y &= \text{partial}x(src.y) \\dst.z &= \text{partial}x(src.z) \\dst.w &= \text{partial}x(src.w)\end{aligned}$$

DDY (Derivative Relative To Y)

$$\begin{aligned}dst.x &= \text{partial}y(src.x) \\dst.y &= \text{partial}y(src.y) \\dst.z &= \text{partial}y(src.z) \\dst.w &= \text{partial}y(src.w)\end{aligned}$$

KILP (Predicated Discard)

discard

PK2H (Pack Two 16-bit Floats)

TBD

PK2US (Pack Two Unsigned 16-bit Scalars)

TBD

PK4B (Pack Four Signed 8-bit Scalars)

TBD

PK4UB (Pack Four Unsigned 8-bit Scalars)

TBD

RFL (Reflection Vector)

$$dst.x = 2 \times (src0.x \times src1.x + src0.y \times src1.y + src0.z \times src1.z) / (src0.x \times src0.x + src0.y \times src0.y + src0.z \times src0.z) \times src0.x$$

$$dst.y = 2 \times (src0.x \times src1.x + src0.y \times src1.y + src0.z \times src1.z) / (src0.x \times src0.x + src0.y \times src0.y + src0.z \times src0.z) \times src0.y$$

$$dst.z = 2 \times (src0.x \times src1.x + src0.y \times src1.y + src0.z \times src1.z) / (src0.x \times src0.x + src0.y \times src0.y + src0.z \times src0.z) \times src0.z$$

$$dst.w = 1$$

Note: Considered for removal.

SEQ (Set On Equal)

$$dst.x = (src0.x == src1.x)?1 : 0$$

$$dst.y = (src0.y == src1.y)?1 : 0$$

$$dst.z = (src0.z == src1.z)?1 : 0$$

$$dst.w = (src0.w == src1.w)?1 : 0$$

SFL (Set On False)

This instruction replicates its result.

$$dst = 0$$

Note: Considered for removal.

SGT (Set On Greater Than)

$$dst.x = (src0.x > src1.x)?1 : 0$$

$$dst.y = (src0.y > src1.y)?1 : 0$$

$$dst.z = (src0.z > src1.z)?1 : 0$$

$$dst.w = (src0.w > src1.w)?1 : 0$$

SIN (Sine)

This instruction replicates its result.

$$dst = \sin src.x$$

SLE (Set On Less Equal Than)

$$\begin{aligned} dst.x &= (src0.x \leq src1.x)?1 : 0 \\ dst.y &= (src0.y \leq src1.y)?1 : 0 \\ dst.z &= (src0.z \leq src1.z)?1 : 0 \\ dst.w &= (src0.w \leq src1.w)?1 : 0 \end{aligned}$$

SNE (Set On Not Equal)

$$\begin{aligned} dst.x &= (src0.x \neq src1.x)?1 : 0 \\ dst.y &= (src0.y \neq src1.y)?1 : 0 \\ dst.z &= (src0.z \neq src1.z)?1 : 0 \\ dst.w &= (src0.w \neq src1.w)?1 : 0 \end{aligned}$$

STR (Set On True)

This instruction replicates its result.

$$dst = 1$$

TEX (Texture Lookup)

$$\begin{aligned} coord &= src0 \\ bias &= 0.0 \end{aligned}$$

$$dst = texture_sample(unit, coord, bias)$$

for array textures $src0.y$ contains the slice for 1D, and $src0.z$ contains the slice for 2D. for shadow textures with no arrays, $src0.z$ contains the slice for 1D.

TXD (Texture Lookup with Derivatives)

$$\begin{aligned} coord &= src0 \\ ddx &= src1 \\ ddy &= src2 \\ bias &= 0.0 \end{aligned}$$

$$dst = texture_sample_deriv(unit, coord, bias, ddx, ddy)$$

TXP (Projective Texture Lookup)

$$\begin{aligned}coord.x &= src0.x/src.w \\ coord.y &= src0.y/src.w \\ coord.z &= src0.z/src.w \\ coord.w &= src0.w \\ bias &= 0.0 \\ dst &= texture_sample(unit, coord, bias)\end{aligned}$$

UP2H (Unpack Two 16-Bit Floats)

TBD

Note: Considered for removal.

UP2US (Unpack Two Unsigned 16-Bit Scalars)

TBD

Note: Considered for removal.

UP4B (Unpack Four Signed 8-Bit Values)

TBD

Note: Considered for removal.

UP4UB (Unpack Four Unsigned 8-Bit Scalars)

TBD

Note: Considered for removal.

X2D (2D Coordinate Transformation)

$$\begin{aligned}dst.x &= src0.x + src1.x \times src2.x + src1.y \times src2.y \\ dst.y &= src0.y + src1.x \times src2.z + src1.y \times src2.w \\ dst.z &= src0.x + src1.x \times src2.x + src1.y \times src2.y \\ dst.w &= src0.y + src1.x \times src2.z + src1.y \times src2.w\end{aligned}$$

Note: Considered for removal.

ARA (Address Register Add)

TBD

Note: Considered for removal.

ARR (Address Register Load With Round)

$$dst.x = round(src.x)$$

$$dst.y = round(src.y)$$

$$dst.z = round(src.z)$$

$$dst.w = round(src.w)$$

BRA (Branch)

pc = target

Note: Considered for removal.

CAL (Subroutine Call)

push(pc) pc = target

RET (Subroutine Call Return)

pc = pop()

SSG (Set Sign)

$$dst.x = (src.x > 0)?1 : (src.x < 0)? -1 : 0$$

$$dst.y = (src.y > 0)?1 : (src.y < 0)? -1 : 0$$

$$dst.z = (src.z > 0)?1 : (src.z < 0)? -1 : 0$$

$$dst.w = (src.w > 0)?1 : (src.w < 0)? -1 : 0$$

CMP (Compare)

$$dst.x = (src0.x < 0)?src1.x : src2.x$$

$$dst.y = (src0.y < 0)?src1.y : src2.y$$

$$dst.z = (src0.z < 0)?src1.z : src2.z$$

$$dst.w = (src0.w < 0)?src1.w : src2.w$$

KIL (Conditional Discard)

$$if(src.x < 0 || src.y < 0 || src.z < 0 || src.w < 0) discardendif$$

SCS (Sine Cosine)

$$dst.x = \cos src.x$$

$$dst.y = \sin src.x$$

$$dst.z = 0$$

$$dst.w = 1$$

TXB (Texture Lookup With Bias)

$$coord.x = src.x$$

$$coord.y = src.y$$

$$coord.z = src.z$$

$$coord.w = 1.0$$

$$bias = src.z$$

$$dst = texture_sample(unit, coord, bias)$$

NRM (3-component Vector Normalise)

$$dst.x = src.x / (src.x \times src.x + src.y \times src.y + src.z \times src.z)$$

$$dst.y = src.y / (src.x \times src.x + src.y \times src.y + src.z \times src.z)$$

$$dst.z = src.z / (src.x \times src.x + src.y \times src.y + src.z \times src.z)$$

$$dst.w = 1$$

DIV (Divide)

$$dst.x = \frac{src0.x}{src1.x}$$

$$dst.y = \frac{src0.y}{src1.y}$$

$$dst.z = \frac{src0.z}{src1.z}$$

$$dst.w = \frac{src0.w}{src1.w}$$

DP2 (2-component Dot Product)

This instruction replicates its result.

$$dst = src0.x \times src1.x + src0.y \times src1.y$$

TXL (Texture Lookup With explicit LOD)

```
coord.x = src0.x
coord.y = src0.y
coord.z = src0.z
coord.w = 1.0
lod = src0.w
dst = texture_sample(unit, coord, lod)
```

BRK (Break)

TBD

IF (If)

TBD

ELSE (Else)

TBD

ENDIF (End If)

TBD

PUSHA (Push Address Register On Stack)

push(src.x) push(src.y) push(src.z) push(src.w)

Note: Considered for cleanup.

Note: Considered for removal.

POPA (Pop Address Register From Stack)

dst.w = pop() dst.z = pop() dst.y = pop() dst.x = pop()

Note: Considered for cleanup.

Note: Considered for removal.

3.2.2 Compute ISA

These opcodes are primarily provided for special-use computational shaders. Support for these opcodes indicated by a special pipe capability bit (TBD).

XXX so let's discuss it, yeah?

CEIL (Ceiling)

$$\begin{aligned}dst.x &= \lceil src.x \rceil \\dst.y &= \lceil src.y \rceil \\dst.z &= \lceil src.z \rceil \\dst.w &= \lceil src.w \rceil\end{aligned}$$

I2F (Integer To Float)

$$\begin{aligned}dst.x &= (float)src.x \\dst.y &= (float)src.y \\dst.z &= (float)src.z \\dst.w &= (float)src.w\end{aligned}$$

NOT (Bitwise Not)

$$\begin{aligned}dst.x &= \text{src.x} \\dst.y &= \text{src.y} \\dst.z &= \text{src.z} \\dst.w &= \text{src.w}\end{aligned}$$

TRUNC (Truncate)

$$\begin{aligned}dst.x &= \text{trunc}(src.x) \\dst.y &= \text{trunc}(src.y) \\dst.z &= \text{trunc}(src.z) \\dst.w &= \text{trunc}(src.w)\end{aligned}$$

SHL (Shift Left)

$$\begin{aligned}dst.x &= src0.x \ll src1.x \\dst.y &= src0.y \ll src1.x \\dst.z &= src0.z \ll src1.x \\dst.w &= src0.w \ll src1.x\end{aligned}$$

SHR (Shift Right)

$$\begin{aligned}dst.x &= src0.x \gg src1.x \\dst.y &= src0.y \gg src1.x \\dst.z &= src0.z \gg src1.x \\dst.w &= src0.w \gg src1.x\end{aligned}$$
AND (Bitwise And)
$$\begin{aligned}dst.x &= src0.xsrc1.x \\dst.y &= src0.ysrc1.y \\dst.z &= src0.zsrc1.z \\dst.w &= src0.wsrc1.w\end{aligned}$$
OR (Bitwise Or)
$$\begin{aligned}dst.x &= src0.x|src1.x \\dst.y &= src0.y|src1.y \\dst.z &= src0.z|src1.z \\dst.w &= src0.w|src1.w\end{aligned}$$
MOD (Modulus)
$$\begin{aligned}dst.x &= src0.x \bmod src1.x \\dst.y &= src0.y \bmod src1.y \\dst.z &= src0.z \bmod src1.z \\dst.w &= src0.w \bmod src1.w\end{aligned}$$
XOR (Bitwise Xor)
$$\begin{aligned}dst.x &= src0.x \oplus src1.x \\dst.y &= src0.y \oplus src1.y \\dst.z &= src0.z \oplus src1.z \\dst.w &= src0.w \oplus src1.w\end{aligned}$$
UCMP (Integer Conditional Move)

$$\begin{aligned}dst.x &= src0.x ? src1.x : src2.x \\dst.y &= src0.y ? src1.y : src2.y \\dst.z &= src0.z ? src1.z : src2.z \\dst.w &= src0.w ? src1.w : src2.w\end{aligned}$$
UARL (Integer Address Register Load)

Moves the contents of the source register, assumed to be an integer, into the destination register, which is assumed to be an address (ADDR) register.

IABS (Integer Absolute Value)
$$\begin{aligned}dst.x &= |src.x| \\dst.y &= |src.y| \\dst.z &= |src.z| \\dst.w &= |src.w|\end{aligned}$$
SAD (Sum Of Absolute Differences)
$$\begin{aligned}dst.x &= |src0.x - src1.x| + src2.x \\dst.y &= |src0.y - src1.y| + src2.y \\dst.z &= |src0.z - src1.z| + src2.z \\dst.w &= |src0.w - src1.w| + src2.w\end{aligned}$$
TXF (Texel Fetch (as per NV_gpu_shader4), extract a single texel)

from a specified texture image. The source sampler may not be a CUBE or SHADOW.

SRC 0 IS A FOUR (component signed integer vector used to)

identify the single texel accessed. 3 components + level.

src 1 is a 3 component constant signed integer vector, with each component only have a range of

(8..+8 (hw only seems to deal with this range, interface)

allows for up to unsigned int).

TXF(uint_vec coord, int_vec offset).

TXQ (Texture Size Query (as per NV_gpu_program4))

retrieve the dimensions of the texture

depending on the target. For 1D (width), 2D/RECT/CUBE

(width, height), 3D (width, height, depth),

1D array (width, layers), 2D array (width, height, layers)

$$\begin{aligned}lod &= src0 \\dst.x &= texture_width(unit, lod) \\dst.y &= texture_height(unit, lod) \\dst.z &= texture_depth(unit, lod)\end{aligned}$$

CONT (Continue)
TBD

Note: Support for CONT is determined by a special capability bit, TGSI_CONT_SUPPORTED. See [Screen](#) for more information.

3.2.3 Geometry ISA

These opcodes are only supported in geometry shaders; they have no meaning in any other type of shader.

EMIT (Emit)
TBD

ENDPRIM (End Primitive)
TBD

3.2.4 GLSL ISA

These opcodes are part of *GLSL*'s opcode set. Support for these opcodes is determined by a special capability bit, GLSL.

BGNLOOP (Begin a Loop)
TBD

BGNSUB (Begin Subroutine)
TBD

ENDLOOP (End a Loop)
TBD

ENDSUB (End Subroutine)
TBD

NOP (No Operation)
Do nothing.

NRM4 (4-component Vector Normalise)

This instruction replicates its result.

$$dst = \frac{src.x}{src.x \times src.x + src.y \times src.y + src.z \times src.z + src.w \times src.w}$$

3.2.5 ps_2_x

XXX wait what

CALLNZ (Subroutine Call If Not Zero)
TBD

IFC (If)
TBD

BREAKC (Break Conditional)
TBD

3.2.6 Double ISA

The double-precision opcodes reinterpret four-component vectors into two-component vectors with doubled precision in each component.

Support for these opcodes is XXX undecided. :T

DADD (Add)

$$\begin{aligned}dst.xy &= src0.xy + src1.xy \\dst.zw &= src0.zw + src1.zw\end{aligned}$$

DDIV (Divide)

$$\begin{aligned}dst.xy &= src0.xy / src1.xy \\dst.zw &= src0.zw / src1.zw\end{aligned}$$

DSEQ (Set on Equal)

$$\begin{aligned}dst.xy &= src0.xy == src1.xy ? 1.0F : 0.0F \\dst.zw &= src0.zw == src1.zw ? 1.0F : 0.0F\end{aligned}$$

DSLTLT (Set on Less than)

$$\begin{aligned}dst.xy &= src0.xy < src1.xy ? 1.0F : 0.0F \\dst.zw &= src0.zw < src1.zw ? 1.0F : 0.0F\end{aligned}$$

DFRAC (Fraction)

$$\begin{aligned}dst.xy &= src.xy - \lfloor src.xy \rfloor \\dst.zw &= src.zw - \lfloor src.zw \rfloor\end{aligned}$$

DFRACEXP (Convert Number to Fractional and Integral Components)

Like the `frexp()` routine in many math libraries, this opcode stores the exponent of its source to `dst0`, and the significand to `dst1`, such that $dst1 \times 2^{dst0} = src$.

$$\begin{aligned}dst0.xy &= exp(src.xy) \\dst1.xy &= frac(src.xy) \\dst0.zw &= exp(src.zw) \\dst1.zw &= frac(src.zw)\end{aligned}$$

DLDEXP (Multiply Number by Integral Power of 2)

This opcode is the inverse of *DFRACEXP*.

$$\begin{aligned}dst.xy &= src0.xy \times 2^{src1.xy} \\dst.zw &= src0.zw \times 2^{src1.zw}\end{aligned}$$

DMIN (Minimum)

$$\begin{aligned}dst.xy &= \min(src0.xy, src1.xy) \\dst.zw &= \min(src0.zw, src1.zw)\end{aligned}$$

DMAX (Maximum)

$$\begin{aligned}dst.xy &= \max(src0.xy, src1.xy) \\dst.zw &= \max(src0.zw, src1.zw)\end{aligned}$$

DMUL (Multiply)

$$\begin{aligned}dst.xy &= src0.xy \times src1.xy \\dst.zw &= src0.zw \times src1.zw\end{aligned}$$

DMAD (Multiply And Add)

$$\begin{aligned}dst.xy &= src0.xy \times src1.xy + src2.xy \\dst.zw &= src0.zw \times src1.zw + src2.zw\end{aligned}$$

DRCP (Reciprocal)

$$\begin{aligned}dst.xy &= \frac{1}{src.xy} \\dst.zw &= \frac{1}{src.zw}\end{aligned}$$

DSQRT (Square Root)

$$\begin{aligned}dst.xy &= \sqrt{src.xy} \\dst.zw &= \sqrt{src.zw}\end{aligned}$$

3.2.7 Resource Sampling Opcodes

Those opcodes follow very closely semantics of the respective Direct3D instructions. If in doubt double check Direct3D documentation.

SAMPLE (Using provided address, sample data from the specified texture using the filtering mode identified by the given sampler. The source data may come from any resource type other than buffers.
SAMPLE dst, address, sampler_view, sampler
 e.g.
SAMPLE TEMP[0], TEMP[1], SVIEW[0], SAMP[0]

SAMPLE_I (Simplified alternative to the **SAMPLE** instruction.)
 Using the provided integer address, **SAMPLE_I** fetches data from the specified sampler view without any filtering. The source data may come from any resource type other than CUBE.

SAMPLE_I dst, address, sampler_view
 e.g.
SAMPLE_I TEMP[0], TEMP[1], SVIEW[0]

The 'address' is specified as unsigned integers. If the 'ADDRESS' IS OUT OF RANGE [0... (# TEXELS (1)) the result of the fetch is always 0 in all components. As such the instruction doesn't honor address wrap modes, in cases where that behavior is desirable 'SAMPLE' instruction should be used.

address.w always provides an unsigned integer mipmap level. If the value is out of the range then the instruction always returns 0 in all components. address.yz are ignored for buffers and 1d textures. address.z is ignored for 1d texture arrays and 2d textures.

For 1D texture arrays address.y provides the array index (also as unsigned integer). If the value is out of the range of available array indices [0... (ARRAY SIZE (1)) then the opcode always returns 0 in all components.

For 2D texture arrays address.z provides the array index, otherwise it exhibits the same behavior as in the case for 1D texture arrays.

The exact semantics of the source address are presented in the table below:

resource type	X	Y	Z	W
(_____)				
PIPE_BUFFER	x			ignored
PIPE_TEXTURE_1D	x			mpl
PIPE_TEXTURE_2D	x	y		mpl
PIPE_TEXTURE_3D	x	y	z	mpl
PIPE_TEXTURE_RECT	x	y		mpl
PIPE_TEXTURE_CUBE	not allowed as source			
PIPE_TEXTURE_1D_ARRAY	x	idx		mpl
PIPE_TEXTURE_2D_ARRAY	x	y	idx	mpl

Where 'mpl' is a mipmap level and 'idx' is the array index.

SAMPLE_I_MS (Just like **SAMPLE_I** but allows fetch data from **MULTI** (sampled surfaces.)

SAMPLE_B (Just like the **SAMPLE** instruction with the exception that an additional bias is applied to the level of detail computed as part of the instruction execution.

SAMPLE_B dst, address, sampler_view, sampler, lod_bias
e.g.

SAMPLE_B TEMP[0], TEMP[1], SVIEW[0], SAMP[0], TEMP[2].x

SAMPLE_C (Similar to the **SAMPLE** instruction but it) performs a comparison filter. The operands to **SAMPLE_C** are identical to **SAMPLE**, except that there is an additional float32 operand, reference value, which must be a register **WITH SINGLE** (component, or a scalar literal.)

SAMPLE_C makes the hardware use the current samplers compare_func (in pipe_sampler_state) to compare reference value against the red component value for the source resource at each texel that the currently configured texture filter covers based on the provided coordinates.

SAMPLE_C dst, address, sampler_view.r, sampler, ref_value
e.g.

SAMPLE_C TEMP[0], TEMP[1], SVIEW[0].r, SAMP[0], TEMP[2].x

SAMPLE_C_LZ (Same as **SAMPLE_C**, but LOD is 0 and derivatives) ARE IGNORED. THE LZ STANDS FOR LEVEL (zero.)

SAMPLE_C_LZ dst, address, sampler_view.r, sampler, ref_value
e.g.

SAMPLE_C_LZ TEMP[0], TEMP[1], SVIEW[0].r, SAMP[0], TEMP[2].x

SAMPLE_D (**SAMPLE_D** is identical to the **SAMPLE** opcode except) that the derivatives for the source address in the x direction and the y direction are provided by extra parameters.

SAMPLE_D dst, address, sampler_view, sampler, der_x, der_y
e.g.

SAMPLE_D TEMP[0], TEMP[1], SVIEW[0], SAMP[0], TEMP[2], TEMP[3]

SAMPLE_L (**SAMPLE_L** is identical to the **SAMPLE** opcode except) that the LOD is provided directly as a scalar value, representing no anisotropy. Source addresses A channel is used as the LOD.

SAMPLE_L dst, address, sampler_view, sampler
e.g.

SAMPLE_L TEMP[0], TEMP[1], SVIEW[0], SAMP[0]

GATHER4 (Gathers the four texels to be used in a bi-linear) filtering operation and packs them into a single register. Only works with 2D, 2D array, cubemaps, and cubemaps arrays. For 2D textures, only the addressing modes of the sampler and the top level of any mip pyramid are used. Set W to zero. It behaves like the **SAMPLE** instruction, but a filtered sample is not generated. The four samples that contribute TO FILTERING ARE PLACED INTO XYZW IN COUNTER (clockwise order,) starting with the (u,v) texture coordinate delta at the FOLLOWING LOCATIONS ((, +), (+, +), (+, -), (-, -), where)

the magnitude of the deltas are half a texel.

SVIEWINFO (query the dimensions of a given sampler view.)

dst receives width, height, depth or array size and number of mipmap levels. The dst can have a writemask which will specify what info is the caller interested in.

```
SVIEWINFO dst, src_mip_level, sampler_view
```

e.g.

```
SVIEWINFO TEMP[0], TEMP[1].x, SVIEW[0]
```

src_mip_level is an unsigned integer scalar. If it's out of range then returns 0 for width, height and depth/array size but the total number of mipmap is still returned correctly for the given sampler view. The returned width, height and depth values are for the mipmap level selected by the src_mip_level and are in the number of texels.

For 1d texture array width is in dst.x, array size is in dst.y and dst.zw are always 0.

SAMPLE_POS (query the position of a given sample.)

dst receives float4 (x, y, 0, 0) indicated where the SAMPLE IS LOCATED. IF THE RESOURCE IS NOT A MULTI (sample) resource and not a render target, the result is 0.

SAMPLE_INFO (dst receives number of samples in x.)

IF THE RESOURCE IS NOT A MULTI (sample resource and) not a render target, the result is 0.

3.2.8 Resource Access Opcodes

LOAD (Fetch data from a shader resource)

Syntax: LOAD dst, resource, address

Example: LOAD TEMP[0], RES[0], TEMP[1]

Using the provided integer address, LOAD fetches data from the specified buffer or texture without any filtering.

The 'address' is specified as a vector of unsigned integers. If the 'address' is out of range the result is unspecified.

Only the first mipmap level of a resource can be read from using this instruction.

For 1D or 2D texture arrays, the array index is provided as an unsigned integer in address.y or address.z, respectively. address.yz are ignored for buffers and 1D textures. address.z is ignored for 1D texture arrays and 2D textures. address.w is always ignored.

STORE (Write data to a shader resource)

Syntax: STORE resource, address, src

Example: STORE RES[0], TEMP[0], TEMP[1]

Using the provided integer address, STORE writes data to the specified buffer or texture.

The 'address' is specified as a vector of unsigned integers. If the 'address' is out of range the result is unspecified.

Only the first mipmap level of a resource can be written to using this instruction.

For 1D or 2D texture arrays, the array index is provided as an unsigned integer in address.y or address.z, respectively. address.yz are ignored for buffers and 1D textures. address.z is ignored for 1D texture arrays and 2D textures. address.w is always ignored.

3.2.9 Inter-thread synchronization opcodes

These opcodes are intended for communication between threads running within the same compute grid. For now they're only valid in compute programs.

MFENCE (Memory fence)

Syntax: MFENCE resource

Example: MFENCE RES[0]

This opcode forces strong ordering between any memory access operations that affect the specified resource. This means that previous loads and stores (and only those) will be performed and visible to other threads before the program execution continues.

LFENCE (Load memory fence)

Syntax: LFENCE resource

Example: LFENCE RES[0]

Similar to MFENCE, but it only affects the ordering of memory loads.

SFENCE (Store memory fence)

Syntax: SFENCE resource

Example: SFENCE RES[0]

Similar to MFENCE, but it only affects the ordering of memory stores.

BARRIER (Thread group barrier)

BARRIER

This opcode suspends the execution of the current thread until all the remaining threads in the working group reach the same point of the program. Results are unspecified if any of the remaining threads terminates or never reaches an executed BARRIER instruction.

3.2.10 Atomic opcodes

These opcodes provide atomic variants of some common arithmetic and logical operations. In this context atomicity means that another concurrent memory access operation that affects the same memory location is guaranteed to be performed strictly before or after the entire execution of the atomic operation.

For the moment they're only valid in compute programs.

ATOMUADD (Atomic integer addition)

Syntax: ATOMUADD dst, resource, offset, src

Example: ATOMUADD TEMP[0], RES[0], TEMP[1], TEMP[2]

The following operation is performed atomically on each component:

$$\begin{aligned} dst_i &= resource[offset]_i \\ resource[offset]_i &= dst_i + src_i \end{aligned}$$

ATOMXCHG (Atomic exchange)

Syntax: ATOMXCHG dst, resource, offset, src

Example: ATOMXCHG TEMP[0], RES[0], TEMP[1], TEMP[2]

The following operation is performed atomically on each component:

$$\begin{aligned}dst_i &= resource[offset]_i \\ resource[offset]_i &= src_i\end{aligned}$$

ATOMCAS (Atomic compare-and-exchange)

Syntax: ATOMCAS *dst*, *resource*, *offset*, *cmp*, *src*

Example: ATOMCAS TEMP[0], RES[0], TEMP[1], TEMP[2], TEMP[3]

The following operation is performed atomically on each component:

$$\begin{aligned}dst_i &= resource[offset]_i \\ resource[offset]_i &= (dst_i == cmp_i ? src_i : dst_i)\end{aligned}$$

ATOMAND (Atomic bitwise And)

Syntax: ATOMAND *dst*, *resource*, *offset*, *src*

Example: ATOMAND TEMP[0], RES[0], TEMP[1], TEMP[2]

The following operation is performed atomically on each component:

$$\begin{aligned}dst_i &= resource[offset]_i \\ resource[offset]_i &= dst_i \& src_i\end{aligned}$$

ATOMOR (Atomic bitwise Or)

Syntax: ATOMOR *dst*, *resource*, *offset*, *src*

Example: ATOMOR TEMP[0], RES[0], TEMP[1], TEMP[2]

The following operation is performed atomically on each component:

$$\begin{aligned}dst_i &= resource[offset]_i \\ resource[offset]_i &= dst_i | src_i\end{aligned}$$

ATOMXOR (Atomic bitwise Xor)

Syntax: ATOMXOR *dst*, *resource*, *offset*, *src*

Example: ATOMXOR TEMP[0], RES[0], TEMP[1], TEMP[2]

The following operation is performed atomically on each component:

$$\begin{aligned}dst_i &= resource[offset]_i \\ resource[offset]_i &= dst_i \oplus src_i\end{aligned}$$

ATOMUMIN (Atomic unsigned minimum)

Syntax: `ATOMUMIN dst, resource, offset, src`

Example: `ATOMUMIN TEMP[0], RES[0], TEMP[1], TEMP[2]`

The following operation is performed atomically on each component:

$$\begin{aligned} dst_i &= resource[offset]_i \\ resource[offset]_i &= (dst_i < src_i ? dst_i : src_i) \end{aligned}$$

ATOMUMAX (Atomic unsigned maximum)

Syntax: `ATOMUMAX dst, resource, offset, src`

Example: `ATOMUMAX TEMP[0], RES[0], TEMP[1], TEMP[2]`

The following operation is performed atomically on each component:

$$\begin{aligned} dst_i &= resource[offset]_i \\ resource[offset]_i &= (dst_i > src_i ? dst_i : src_i) \end{aligned}$$

ATOMIMIN (Atomic signed minimum)

Syntax: `ATOMIMIN dst, resource, offset, src`

Example: `ATOMIMIN TEMP[0], RES[0], TEMP[1], TEMP[2]`

The following operation is performed atomically on each component:

$$\begin{aligned} dst_i &= resource[offset]_i \\ resource[offset]_i &= (dst_i < src_i ? dst_i : src_i) \end{aligned}$$

ATOMIMAX (Atomic signed maximum)

Syntax: `ATOMIMAX dst, resource, offset, src`

Example: `ATOMIMAX TEMP[0], RES[0], TEMP[1], TEMP[2]`

The following operation is performed atomically on each component:

$$\begin{aligned} dst_i &= resource[offset]_i \\ resource[offset]_i &= (dst_i > src_i ? dst_i : src_i) \end{aligned}$$

3.3 Explanation of symbols used

3.3.1 Functions

$|x|$ Absolute value of x .

$\lceil x \rceil$ Ceiling of x .

clamp(x,y,z) Clamp x between y and z. $(x < y) ? y : (x > z) ? z : x$

$\lfloor x \rfloor$ Floor of x .

$\log_2 x$ Logarithm of x , base 2.

max(x,y) Maximum of x and y. $(x > y) ? x : y$

min(x,y) Minimum of x and y. $(x < y) ? x : y$

partialx(x) Derivative of x relative to fragment's X.

partially(x) Derivative of x relative to fragment's Y.

pop() Pop from stack.

x^y x to the power y .

push(x) Push x on stack.

round(x) Round x.

trunc(x) Truncate x, i.e. drop the fraction bits.

3.3.2 Keywords

discard Discard fragment.

pc Program counter.

target Label of target instruction.

3.4 Other tokens

3.4.1 Declaration

Declares a register that is will be referenced as an operand in Instruction tokens.

File field contains register file that is being declared and is one of TGSI_FILE.

UsageMask field specifies which of the register components can be accessed and is one of TGSI_WRITEMASK.

The Local flag specifies that a given value isn't intended for subroutine parameter passing and, as a result, the implementation isn't required to give any guarantees of it being preserved across subroutine boundaries. As it's merely a compiler hint, the implementation is free to ignore it.

If Dimension flag is set to 1, a Declaration Dimension token follows.

If Semantic flag is set to 1, a Declaration Semantic token follows.

If Interpolate flag is set to 1, a Declaration Interpolate token follows.

If file is TGSI_FILE_RESOURCE, a Declaration Resource token follows.

3.4.2 Declaration Semantic

Vertex and fragment shader input and output registers may be labeled with semantic information consisting of a name and index.

Follows Declaration token if Semantic bit is set.

Since its purpose is to link a shader with other stages of the pipeline, it is valid to follow only those Declaration tokens that declare a register either in INPUT or OUTPUT file.

SemanticName field contains the semantic name of the register being declared. There is no default value.

SemanticIndex is an optional subscript that can be used to distinguish different register declarations with the same semantic name. The default value is 0.

The meanings of the individual semantic names are explained in the following sections.

TGSI_SEMANTIC_POSITION

For vertex shaders, TGSI_SEMANTIC_POSITION indicates the vertex shader output register which contains the homogeneous vertex position in the clip space coordinate system. After clipping, the X, Y and Z components of the vertex will be divided by the W value to get normalized device coordinates.

For fragment shaders, TGSI_SEMANTIC_POSITION is used to indicate that fragment shader input contains the fragment's window position. The X component starts at zero and always increases from left to right. The Y component starts at zero and always increases but Y=0 may either indicate the top of the window or the bottom depending on the fragment coordinate origin convention (see TGSI_PROPERTY_FS_COORD_ORIGIN). The Z coordinate ranges from 0 to 1 to represent depth from the front to the back of the Z buffer. The W component contains the reciprocal of the interpolated vertex position W component.

Fragment shaders may also declare an output register with TGSI_SEMANTIC_POSITION. Only the Z component is writable. This allows the fragment shader to change the fragment's Z position.

TGSI_SEMANTIC_COLOR

For vertex shader outputs or fragment shader inputs/outputs, this label indicates that the register contains an R,G,B,A color.

Several shader inputs/outputs may contain colors so the semantic index is used to distinguish them. For example, color[0] may be the diffuse color while color[1] may be the specular color.

This label is needed so that the flat/smooth shading can be applied to the right interpolants during rasterization.

TGSI_SEMANTIC_BCOLOR

Back-facing colors are only used for back-facing polygons, and are only valid in vertex shader outputs. After rasterization, all polygons are front-facing and COLOR and BCOLOR end up occupying the same slots in the fragment shader, so all BCOLORs effectively become regular COLORS in the fragment shader.

TGSI_SEMANTIC_FOG

Vertex shader inputs and outputs and fragment shader inputs may be labeled with TGSI_SEMANTIC_FOG to indicate that the register contains a fog coordinate in the form (F, 0, 0, 1). Typically, the fragment shader will use the fog coordinate to compute a fog blend factor which is used to blend the normal fragment color with a constant fog color.

Only the first component matters when writing from the vertex shader; the driver will ensure that the coordinate is in this format when used as a fragment shader input.

TGSI_SEMANTIC_PSIZE

Vertex shader input and output registers may be labeled with `TGSI_SEMANTIC_PSIZE` to indicate that the register contains a point size in the form (S, 0, 0, 1). The point size controls the width or diameter of points for rasterization. This label cannot be used in fragment shaders.

When using this semantic, be sure to set the appropriate state in the *Rasterizer* first.

TGSI_SEMANTIC_GENERIC

All vertex/fragment shader inputs/outputs not labeled with any other semantic label can be considered to be generic attributes. Typical uses of generic inputs/outputs are texcoords and user-defined values.

TGSI_SEMANTIC_NORMAL

Indicates that a vertex shader input is a normal vector. This is typically only used for legacy graphics APIs.

TGSI_SEMANTIC_FACE

This label applies to fragment shader inputs only and indicates that the register contains front/back-face information of the form (F, 0, 0, 1). The first component will be positive when the fragment belongs to a front-facing polygon, and negative when the fragment belongs to a back-facing polygon.

TGSI_SEMANTIC_EDGEFLAG

For vertex shaders, this semantic label indicates that an input or output is a boolean edge flag. The register layout is [F, x, x, x] where F is 0.0 or 1.0 and x = don't care. Normally, the vertex shader simply copies the edge flag input to the edgeflag output.

Edge flags are used to control which lines or points are actually drawn when the polygon mode converts triangles/quads/polygons into points or lines.

TGSI_SEMANTIC_STENCIL

For fragment shaders, this semantic label indicates that an output is a writable stencil reference value. Only the Y component is writable. This allows the fragment shader to change the fragments stencilref value.

3.4.3 Declaration Interpolate

This token is only valid for fragment shader INPUT declarations.

The Interpolate field specifies the way input is being interpolated by the rasteriser and is one of `TGSI_INTERPOLATE_*`.

The CylindricalWrap bitfield specifies which register components should be subject to cylindrical wrapping when interpolating by the rasteriser. If `TGSI_CYLINDRICAL_WRAP_X` is set to 1, the X component should be interpolated according to cylindrical wrapping rules.

3.4.4 Declaration Sampler View

Follows Declaration token if file is TGSI_FILE_SAMPLER_VIEW.

DCL SVIEW[#], resource, type(s)

Declares a shader input sampler view and assigns it to a SVIEW[#] register.

resource can be one of BUFFER, 1D, 2D, 3D, 1DArray and 2DArray.

type must be 1 or 4 entries (if specifying on a per-component level) out of UNORM, SNORM, SINT, UINT and FLOAT.

3.4.5 Declaration Resource

Follows Declaration token if file is TGSI_FILE_RESOURCE.

DCL RES[#], resource [, WR] [, RAW]

Declares a shader input resource and assigns it to a RES[#] register.

resource can be one of BUFFER, 1D, 2D, 3D, CUBE, 1DArray and 2DArray.

If the RAW keyword is not specified, the texture data will be subject to conversion, swizzling and scaling as required to yield the specified data type from the physical data format of the bound resource.

If the RAW keyword is specified, no channel conversion will be performed: the values read for each of the channels (X,Y,Z,W) will correspond to consecutive words in the same order and format they're found in memory. No element-to-address conversion will be performed either: the value of the provided X coordinate will be interpreted in byte units instead of texel units. The result of accessing a misaligned address is undefined.

Usage of the STORE opcode is only allowed if the WR (writable) flag is set.

3.4.6 Properties

Properties are general directives that apply to the whole TGSI program.

FS_COORD_ORIGIN

Specifies the fragment shader TGSI_SEMANTIC_POSITION coordinate origin. The default value is UPPER_LEFT.

If UPPER_LEFT, the position will be (0,0) at the upper left corner and increase downward and rightward. If LOWER_LEFT, the position will be (0,0) at the lower left corner and increase upward and rightward.

OpenGL defaults to LOWER_LEFT, and is configurable with the GL_ARB_fragment_coord_conventions extension.

DirectX 9/10 use UPPER_LEFT.

FS_COORD_PIXEL_CENTER

Specifies the fragment shader TGSI_SEMANTIC_POSITION pixel center convention. The default value is HALF_INTEGER.

If HALF_INTEGER, the fractionary part of the position will be 0.5 If INTEGER, the fractionary part of the position will be 0.0

Note that this does not affect the set of fragments generated by rasterization, which is instead controlled by `gl_rasterization_rules` in the rasterizer.

OpenGL defaults to `HALF_INTEGER`, and is configurable with the `GL_ARB_fragment_coord_conventions` extension.

DirectX 9 uses `INTEGER`. DirectX 10 uses `HALF_INTEGER`.

FS_COLOR0_WRITES_ALL_CBUFS

Specifies that writes to the fragment shader color 0 are replicated to all bound cbufs. This facilitates OpenGL's `fragColor` output vs `fragData[0]` where `fragData` is directed to a single color buffer, but `fragColor` is broadcast.

VS_PROHIBIT_UCPS

If this property is set on the program bound to the shader stage before the fragment shader, user clip planes should have no effect (be disabled) even if that shader does not write to any clip distance outputs and the rasterizer's `clip_plane_enable` is non-zero. This property is only supported by drivers that also support shader clip distance outputs. This is useful for APIs that don't have UCPS and where clip distances written by a shader cannot be disabled.

3.5 Texture Sampling and Texture Formats

This table shows how texture image components are returned as (x,y,z,w) tuples by TGSI texture instructions, such as `TEX`, `TXD`, and `TXP`. For reference, OpenGL and Direct3D conventions are shown as well.

Texture Components	Gallium	OpenGL	Direct3D 9
R	(r, 0, 0, 1)	(r, 0, 0, 1)	(r, 1, 1, 1)
RG	(r, g, 0, 1)	(r, g, 0, 1)	(r, g, 1, 1)
RGB	(r, g, b, 1)	(r, g, b, 1)	(r, g, b, 1)
RGBA	(r, g, b, a)	(r, g, b, a)	(r, g, b, a)
A	(0, 0, 0, a)	(0, 0, 0, a)	(0, 0, 0, a)
L	(l, l, l, 1)	(l, l, l, 1)	(l, l, l, 1)
LA	(l, l, l, a)	(l, l, l, a)	(l, l, l, a)
I	(i, i, i, i)	(i, i, i, i)	N/A
UV	XXX TBD	(0, 0, 0, 1) ¹	(u, v, 1, 1)
Z	XXX TBD	(z, z, z, 1) ²	(0, z, 0, 1)
S	(s, s, s, s)	unknown	unknown

¹http://www.opengl.org/registry/specs/ATI/envmap_bumpmap.txt

²the default is $(z, z, z, 1)$ but may also be $(0, 0, 0, z)$ or (z, z, z, z) depending on the value of `GL_DEPTH_TEXTURE_MODE`.

A screen is an object representing the context-independent part of a device.

4.1 Flags and enumerations

XXX some of these don't belong in this section.

4.1.1 PIPE_CAP_*

Capability queries return information about the features and limits of the driver/GPU. For floating-point values, use *get_paramf*, and for boolean or integer values, use *get_param*.

The integer capabilities:

- **PIPE_CAP_NPOT_TEXTURES:** Whether *NPOT* textures may have repeat modes, normalized coordinates, and mipmaps.
- **PIPE_CAP_TWO_SIDED_STENCIL:** Whether the stencil test can also affect back-facing polygons.
- **PIPE_CAP_MAX_DUAL_SOURCE_RENDER_TARGETS:** How many dual-source blend RTs are support. *Blend* for more information.
- **PIPE_CAP_ANISOTROPIC_FILTER:** Whether textures can be filtered anisotropically.
- **PIPE_CAP_POINT_SPRITE:** Whether point sprites are available.
- **PIPE_CAP_MAX_RENDER_TARGETS:** The maximum number of render targets that may be bound.
- **PIPE_CAP_OCCLUSION_QUERY:** Whether occlusion queries are available.
- **PIPE_CAP_TIMER_QUERY:** Whether timer queries are available.
- **PIPE_CAP_TEXTURE_SHADOW_MAP:** indicates whether the fragment shader hardware can do the depth texture / Z comparison operation in TEX instructions for shadow testing.
- **PIPE_CAP_TEXTURE_SWIZZLE:** Whether swizzling through sampler views is supported.
- **PIPE_CAP_MAX_TEXTURE_2D_LEVELS:** The maximum number of mipmap levels available for a 2D texture.
- **PIPE_CAP_MAX_TEXTURE_3D_LEVELS:** The maximum number of mipmap levels available for a 3D texture.
- **PIPE_CAP_MAX_TEXTURE_CUBE_LEVELS:** The maximum number of mipmap levels available for a cube-map.

- `PIPE_CAP_TEXTURE_MIRROR_CLAMP`: Whether mirrored texture coordinates with clamp are supported.
- `PIPE_CAP_BLEND_EQUATION_SEPARATE`: Whether alpha blend equations may be different from color blend equations, in *Blend* state.
- `PIPE_CAP_SM3`: Whether the vertex shader and fragment shader support equivalent opcodes to the Shader Model 3 specification. XXX oh god this is horrible
- `PIPE_CAP_MAX_STREAM_OUTPUT_BUFFERS`: The maximum number of stream buffers.
- `PIPE_CAP_PRIMITIVE_RESTART`: Whether primitive restart is supported.
- `PIPE_CAP_MAX_COMBINED_SAMPLERS`: The total number of samplers accessible from the vertex and fragment shader, inclusive.
- `PIPE_CAP_INDEP_BLEND_ENABLE`: Whether per-rendertarget blend enabling and channel masks are supported. If 0, then the first rendertarget's blend mask is replicated across all MRTs.
- `PIPE_CAP_INDEP_BLEND_FUNC`: Whether per-rendertarget blend functions are available. If 0, then the first rendertarget's blend functions affect all MRTs.
- `PIPE_CAP_DEPTHSTENCIL_CLEAR_SEPARATE`: Whether clearing only depth or only stencil in a combined depth-stencil buffer is supported.
- `PIPE_CAP_MAX_TEXTURE_ARRAY_LAYERS`: The maximum number of texture array layers supported. If 0, the array textures are not supported at all and the `ARRAY` texture targets are invalid.
- `PIPE_CAP_TGSI_FS_COORD_ORIGIN_UPPER_LEFT`: Whether the TGSI property `FS_COORD_ORIGIN` with value `UPPER_LEFT` is supported.
- `PIPE_CAP_TGSI_FS_COORD_ORIGIN_LOWER_LEFT`: Whether the TGSI property `FS_COORD_ORIGIN` with value `LOWER_LEFT` is supported.
- `PIPE_CAP_TGSI_FS_COORD_PIXEL_CENTER_HALF_INTEGER`: Whether the TGSI property `FS_COORD_PIXEL_CENTER` with value `HALF_INTEGER` is supported.
- `PIPE_CAP_TGSI_FS_COORD_PIXEL_CENTER_INTEGER`: Whether the TGSI property `FS_COORD_PIXEL_CENTER` with value `INTEGER` is supported.
- `PIPE_CAP_DEPTH_CLIP_DISABLE`: Whether the driver is capable of disabling depth clipping (through `pipe_rasterizer_state`)
- `PIPE_CAP_SHADER_STENCIL_EXPORT`: Whether a stencil reference value can be written from a fragment shader.
- `PIPE_CAP_TGSI_INSTANCEID`: Whether `TGSI_SEMANTIC_INSTANCEID` is supported in the vertex shader.
- `PIPE_CAP_VERTEX_ELEMENT_INSTANCE_DIVISOR`: Whether the driver supports per-instance vertex attribs.
- `PIPE_CAP_FRAGMENT_COLOR_CLAMPED`: Whether fragment color clamping is supported. That is, is the `pipe_rasterizer_state::clamp_fragment_color` flag supported by the driver? If not, the state tracker will insert clamping code into the fragment shaders when needed.
- `PIPE_CAP_MIXED_COLORBUFFER_FORMATS`: Whether mixed colorbuffer formats are supported, e.g. `RGBA8` and `RGBA32F` as the first and second colorbuffer, resp.
- `PIPE_CAP_VERTEX_COLOR_UNCLAMPED`: Whether the driver is capable of outputting unclamped vertex colors from a vertex shader. If unsupported, the vertex colors are always clamped. This is the default for DX9 hardware.
- `PIPE_CAP_VERTEX_COLOR_CLAMPED`: Whether the driver is capable of clamping vertex colors when they come out of a vertex shader, as specified by the `pipe_rasterizer_state::clamp_vertex_color` flag. If unsupported,

the vertex colors are never clamped. This is the default for DX10 hardware. If both clamped and unclamped CAPs are supported, the clamping can be controlled through `pipe_rasterizer_state`. If the driver cannot do vertex color clamping, the state tracker may insert clamping code into the vertex shader.

- `PIPE_CAP_GLSL_FEATURE_LEVEL`: Whether the driver supports features equivalent to a specific GLSL version. E.g. for GLSL 1.3, report 130.
- `PIPE_CAP_QUADS_FOLLOW_PROVOKING_VERTEX_CONVENTION`: Whether quads adhere to the `flat-shade_first` setting in `pipe_rasterizer_state`.
- `PIPE_CAP_USER_VERTEX_BUFFERS`: Whether the driver supports user vertex buffers. If not, the state tracker must upload all data which is not in hw resources.
- `PIPE_CAP_VERTEX_BUFFER_OFFSET_4BYTE_ALIGNED_ONLY`: This CAP describes a hw limitation. If true, `pipe_vertex_buffer::buffer_offset` must always be aligned to 4. If false, there are no restrictions on the offset.
- `PIPE_CAP_VERTEX_BUFFER_STRIDE_4BYTE_ALIGNED_ONLY`: This CAP describes a hw limitation. If true, `pipe_vertex_buffer::stride` must always be aligned to 4. If false, there are no restrictions on the stride.
- `PIPE_CAP_VERTEX_ELEMENT_SRC_OFFSET_4BYTE_ALIGNED_ONLY`: This CAP describes a hw limitation. If true, `pipe_vertex_element::src_offset` must always be aligned to 4. If false, there are no restrictions on `src_offset`.
- `PIPE_CAP_COMPUTE`: Whether the implementation supports the compute entry points defined in `pipe_context` and `pipe_screen`.
- `PIPE_CAP_USER_INDEX_BUFFERS`: Whether user index buffers are supported. If not, the state tracker must upload all indices which are not in hw resources.
- `PIPE_CAP_USER_CONSTANT_BUFFERS`: Whether user constant buffers are supported. If not, the state tracker must upload constants which are not in hw resources.
- `PIPE_CAP_CONSTANT_BUFFER_OFFSET_ALIGNMENT`: Describes the required alignment of `pipe_constant_buffer::buffer_offset`.
- `PIPE_CAP_START_INSTANCE`: Whether the driver supports `pipe_draw_info::start_instance`.
- `PIPE_CAP_QUERY_TIMESTAMP`: Whether `PIPE_QUERY_TIMESTAMP` and the `pipe_screen::get_timestamp` hook are implemented.

4.1.2 PIPE_CAPF_*

The floating-point capabilities are:

- `PIPE_CAPF_MAX_LINE_WIDTH`: The maximum width of a regular line.
- `PIPE_CAPF_MAX_LINE_WIDTH_AA`: The maximum width of a smoothed line.
- `PIPE_CAPF_MAX_POINT_WIDTH`: The maximum width and height of a point.
- `PIPE_CAPF_MAX_POINT_WIDTH_AA`: The maximum width and height of a smoothed point.
- `PIPE_CAPF_MAX_TEXTURE_ANISOTROPY`: The maximum level of anisotropy that can be applied to anisotropically filtered textures.
- `PIPE_CAPF_MAX_TEXTURE_LOD_BIAS`: The maximum *LOD* bias that may be applied to filtered textures.
- `PIPE_CAPF_GUARD_BAND_LEFT`, `PIPE_CAPF_GUARD_BAND_TOP`, `PIPE_CAPF_GUARD_BAND_RIGHT`, `PIPE_CAPF_GUARD_BAND_BOTTOM`: **TODO**

4.1.3 PIPE_SHADER_CAP_*

These are per-shader-stage capability queries. Different shader stages may support different features.

- `PIPE_SHADER_CAP_MAX_INSTRUCTIONS`: The maximum number of instructions.
- `PIPE_SHADER_CAP_MAX_ALU_INSTRUCTIONS`: The maximum number of arithmetic instructions.
- `PIPE_SHADER_CAP_MAX_TEX_INSTRUCTIONS`: The maximum number of texture instructions.
- `PIPE_SHADER_CAP_MAX_TEX_INDIRECTIONS`: The maximum number of texture indirections.
- `PIPE_SHADER_CAP_MAX_CONTROL_FLOW_DEPTH`: The maximum nested control flow depth.
- `PIPE_SHADER_CAP_MAX_INPUTS`: The maximum number of input registers.
- `PIPE_SHADER_CAP_MAX_CONSTS`: The maximum number of constants.
- `PIPE_SHADER_CAP_MAX_CONST_BUFFERS`: Maximum number of constant buffers that can be bound to any shader stage using `set_constant_buffer`. If 0 or 1, the pipe will only permit binding one constant buffer per shader, and the shaders will not permit two-dimensional access to constants.

If a value greater than 0 is returned, the driver can have multiple constant buffers bound to shader stages. The `CONST` register file can be accessed with two-dimensional indices, like in the example below.

```
DCL CONST[0][0..7] # declare first 8 vectors of constbuf 0
DCL CONST[3][0] # declare first vector of constbuf 3
MOV OUT[0], CONST[0][3] # copy vector 3 of constbuf 0
```

For backwards compatibility, one-dimensional access to `CONST` register file is still supported. In that case, the `constbuf` index is assumed to be 0.

- `PIPE_SHADER_CAP_MAX_TEMPS`: The maximum number of temporary registers.
- `PIPE_SHADER_CAP_MAX_ADDRS`: The maximum number of address registers.
- `PIPE_SHADER_CAP_MAX_PREDS`: The maximum number of predicate registers.
- `PIPE_SHADER_CAP_TGSI_CONT_SUPPORTED`: Whether the continue opcode is supported.
- `PIPE_SHADER_CAP_INDIRECT_INPUT_ADDR`: Whether indirect addressing of the input file is supported.
- `PIPE_SHADER_CAP_INDIRECT_OUTPUT_ADDR`: Whether indirect addressing of the output file is supported.
- `PIPE_SHADER_CAP_INDIRECT_TEMP_ADDR`: Whether indirect addressing of the temporary file is supported.
- `PIPE_SHADER_CAP_INDIRECT_CONST_ADDR`: Whether indirect addressing of the constant file is supported.
- `PIPE_SHADER_CAP_SUBROUTINES`: Whether subroutines are supported, i.e. `BGNSUB`, `ENDSUB`, `CAL`, and `RET`, including `RET` in the main block.
- `PIPE_SHADER_CAP_INTEGERS`: Whether integer opcodes are supported. If unsupported, only float opcodes are supported.
- `PIPE_SHADER_CAP_MAX_TEXTURE_SAMPLERS`: The maximum number of texture samplers.
- `PIPE_SHADER_CAP_PREFERRED_IR`: Preferred representation of the program. It should be one of the `pipe_shader_ir` enum values.

4.1.4 PIPE_COMPUTE_CAP_*

Compute-specific capabilities. They can be queried using `pipe_screen::get_compute_param`.

- `PIPE_COMPUTE_CAP_IR_TARGET`: A description of the target as a target triple specification of the form `processor-manufacturer-os` that will be passed on to the compiler. This CAP is only relevant for drivers that specify `PIPE_SHADER_IR_LLVM` for their preferred IR. Value type: null-terminated string.
- `PIPE_COMPUTE_CAP_GRID_DIMENSION`: Number of supported dimensions for grid and block coordinates. Value type: `uint64_t`.
- `PIPE_COMPUTE_CAP_MAX_GRID_SIZE`: Maximum grid size in block units. Value type: `uint64_t []`.
- `PIPE_COMPUTE_CAP_MAX_BLOCK_SIZE`: Maximum block size in thread units. Value type: `uint64_t []`.
- `PIPE_COMPUTE_CAP_MAX_THREADS_PER_BLOCK`: Maximum number of threads that a single block can contain. Value type: `uint64_t`. This may be less than the product of the components of `MAX_BLOCK_SIZE` and is usually limited by the number of threads that can be resident simultaneously on a compute unit.
- `PIPE_COMPUTE_CAP_MAX_GLOBAL_SIZE`: Maximum size of the GLOBAL resource. Value type: `uint64_t`.
- `PIPE_COMPUTE_CAP_MAX_LOCAL_SIZE`: Maximum size of the LOCAL resource. Value type: `uint64_t`.
- `PIPE_COMPUTE_CAP_MAX_PRIVATE_SIZE`: Maximum size of the PRIVATE resource. Value type: `uint64_t`.
- `PIPE_COMPUTE_CAP_MAX_INPUT_SIZE`: Maximum size of the INPUT resource. Value type: `uint64_t`.

4.1.5 PIPE_BIND_*

These flags indicate how a resource will be used and are specified at resource creation time. Resources may be used in different roles during their lifecycle. Bind flags are cumulative and may be combined to create a resource which can be used for multiple things. Depending on the pipe driver's memory management and these bind flags, resources might be created and handled quite differently.

- `PIPE_BIND_RENDER_TARGET`: A color buffer or pixel buffer which will be rendered to. Any surface/resource attached to `pipe_framebuffer_state::cbufs` must have this flag set.
- `PIPE_BIND_DEPTH_STENCIL`: A depth (Z) buffer and/or stencil buffer. Any depth/stencil surface/resource attached to `pipe_framebuffer_state::zbuf` must have this flag set.
- `PIPE_BIND_BLENDABLE`: Used in conjunction with `PIPE_BIND_RENDER_TARGET` to query whether a device supports blending for a given format. If this flag is set, surface creation may fail if blending is not supported for the specified format. If it is not set, a driver may choose to ignore blending on surfaces with formats that would require emulation.
- `PIPE_BIND_DISPLAY_TARGET`: A surface that can be presented to screen. Arguments to `pipe_screen::flush_front_buffer` must have this flag set.
- `PIPE_BIND_SAMPLER_VIEW`: A texture that may be sampled from in a fragment or vertex shader.
- `PIPE_BIND_VERTEX_BUFFER`: A vertex buffer.
- `PIPE_BIND_INDEX_BUFFER`: An vertex index/element buffer.
- `PIPE_BIND_CONSTANT_BUFFER`: A buffer of shader constants.
- `PIPE_BIND_TRANSFER_WRITE`: A transfer object which will be written to.
- `PIPE_BIND_TRANSFER_READ`: A transfer object which will be read from.
- `PIPE_BIND_STREAM_OUTPUT`: A stream output buffer.
- `PIPE_BIND_CUSTOM`:

- `PIPE_BIND_SCANOUT`: A front color buffer or scanout buffer.
- `PIPE_BIND_SHARED`: A sharable buffer that can be given to another process.
- `PIPE_BIND_GLOBAL`: A buffer that can be mapped into the global address space of a compute program.
- `PIPE_BIND_SHADER_RESOURCE`: A buffer or texture that can be bound to the graphics pipeline as a shader resource.
- `PIPE_BIND_COMPUTE_RESOURCE`: A buffer or texture that can be bound to the compute program as a shader resource.

4.1.6 PIPE_USAGE_*

The `PIPE_USAGE` enums are hints about the expected usage pattern of a resource.

- `PIPE_USAGE_DEFAULT`: Expect many uploads to the resource, intermixed with draws.
- `PIPE_USAGE_DYNAMIC`: Expect many uploads to the resource, intermixed with draws.
- `PIPE_USAGE_STATIC`: Same as immutable (?)
- `PIPE_USAGE_IMMUTABLE`: Resource will not be changed after first upload.
- `PIPE_USAGE_STREAM`: Upload will be followed by draw, followed by upload, ...

4.2 Methods

XXX to-do

4.2.1 get_name

Returns an identifying name for the screen.

4.2.2 get_vendor

Returns the screen vendor.

4.2.3 get_param

Get an integer/boolean screen parameter.

param is one of the `PIPE_CAP_*` names.

4.2.4 get_paramf

Get a floating-point screen parameter.

param is one of the `PIPE_CAP_*` names.

4.2.5 context_create

Create a pipe_context.

priv is private data of the caller, which may be put to various unspecified uses, typically to do with implementing swapbuffers and/or front-buffer rendering.

4.2.6 is_format_supported

Determine if a resource in the given format can be used in a specific manner.

format the resource format

target one of the PIPE_TEXTURE_x flags

sample_count the number of samples. 0 and 1 mean no multisampling, the maximum allowed legal value is 32.

bindings is a bitmask of *PIPE_BIND_** flags.

geom_flags is a bitmask of PIPE_TEXTURE_GEOM_x flags.

Returns TRUE if all usages can be satisfied.

4.2.7 resource_create

Create a new resource from a template. The following fields of the pipe_resource must be specified in the template:

target one of the pipe_texture_target enums. Note that PIPE_BUFFER and PIPE_TEXTURE_X are not really fundamentally different. Modern APIs allow using buffers as shader resources.

format one of the pipe_format enums.

width0 the width of the base mip level of the texture or size of the buffer.

height0 the height of the base mip level of the texture (1 for 1D or 1D array textures).

depth0 the depth of the base mip level of the texture (1 for everything else).

array_size the array size for 1D and 2D array textures. For cube maps this must be 6, for other textures 1.

last_level the last mip map level present.

nr_samples the nr of msaa samples. 0 (or 1) specifies a resource which isn't multisampled.

usage one of the PIPE_USAGE flags.

bind bitmask of the PIPE_BIND flags.

flags bitmask of PIPE_RESOURCE_FLAG flags.

4.2.8 resource_destroy

Destroy a resource. A resource is destroyed if it has no more references.

4.2.9 get_timestamp

Query a timestamp in nanoseconds. The returned value should match PIPE_QUERY_TIMESTAMP. This function returns immediately and doesn't wait for rendering to complete (which cannot be achieved with queries).

Resources and derived objects

Resources represent objects that hold data: textures and buffers.

They are mostly modelled after the resources in Direct3D 10/11, but with a different transfer/update mechanism, and more features for OpenGL support.

Resources can be used in several ways, and it is required to specify all planned uses through an appropriate set of bind flags.

TODO: write much more on resources

5.1 Transfers

Transfers are the mechanism used to access resources with the CPU.

OpenGL: OpenGL supports mapping buffers and has inline transfer functions for both buffers and textures

D3D11: D3D11 lacks transfers, but has special resource types that are mappable to the CPU address space

TODO: write much more on transfers

5.2 Resource targets

Resource targets determine the type of a resource.

Note that drivers may not actually have the restrictions listed regarding coordinate normalization and wrap modes, and in fact efficient OpenCL support will probably require drivers that don't have any of them, which will probably be advertised with an appropriate cap.

TODO: document all targets. Note that both 3D and cube have restrictions that depend on the hardware generation.

TODO: can buffers have a non-R8 format?

5.2.1 PIPE_BUFFER

Buffer resource: can be used as a vertex, index, constant buffer (appropriate bind flags must be requested).

They can be bound to stream output if supported. TODO: what about the restrictions lifted by the several later GL transform feedback extensions? How does one advertise that in Gallium?

They can also be bound to a shader stage as usual. TODO: are all drivers supposed to support this? how does this work exactly? are there size limits?

They can be also be bound to the framebuffer as usual. TODO: are all drivers supposed to support this? how does this work exactly? are there size limits? TODO: is there any chance of supporting GL pixel buffer object acceleration with this?

- depth0 must be 1
- last_level must be 0
- TODO: what about normalization?
- TODO: wrap modes/other sampling state?
- TODO: are arbitrary formats supported? in which cases?

OpenGL: vertex buffers in GL 1.5 or GL_ARB_vertex_buffer_object

- Binding to stream out requires GL 3.0 or GL_NV_transform_feedback
- Binding as constant buffers requires GL 3.1 or GL_ARB_uniform_buffer_object
- Binding to a sampling stage requires GL 3.1 or GL_ARB_texture_buffer_object
- TODO: can they be bound to an FBO?

D3D11: buffer resources - Binding to a render target requires D3D_FEATURE_LEVEL_10_0

5.2.2 PIPE_TEXTURE_1D

1D surface accessed with normalized coordinates.

UNIMPLEMENTED: 1D texture arrays not supported

- **If PIPE_CAP_NPOT_TEXTURES is not supported,** width must be a power of two
- height0 must be 1
- depth0 must be 1
- Mipmaps can be used
- Must use normalized coordinates

OpenGL: GL_TEXTURE_1D in GL 1.0

- PIPE_CAP_NPOT_TEXTURES is equivalent to GL 2.0 or GL_ARB_texture_non_power_of_two

D3D11: 1D textures in D3D_FEATURE_LEVEL_10_0

5.2.3 PIPE_TEXTURE_RECT

2D surface with OpenGL GL_TEXTURE_RECTANGLE semantics.

- depth0 must be 1
- last_level must be 0
- Must use unnormalized coordinates
- Must use a clamp wrap mode

OpenGL: GL_TEXTURE_RECTANGLE in GL 3.1 or GL_ARB_texture_rectangle or GL_NV_texture_rectangle

OpenCL: can create OpenCL images based on this, that can then be sampled arbitrarily

D3D11: not supported (only PIPE_TEXTURE_2D with normalized coordinates is supported)

5.2.4 PIPE_TEXTURE_2D

2D surface accessed with normalized coordinates.

UNIMPLEMENTED: 2D texture arrays not supported

- **If PIPE_CAP_NPOT_TEXTURES is not supported**, width and height must be powers of two
- depth0 must be 1
- Mipmaps can be used
- Must use normalized coordinates
- No special restrictions on wrap modes

OpenGL: GL_TEXTURE_2D in GL 1.0

- PIPE_CAP_NPOT_TEXTURES is equivalent to GL 2.0 or GL_ARB_texture_non_power_of_two

OpenCL: can create OpenCL images based on this, that can then be sampled arbitrarily

D3D11: 2D textures

- PIPE_CAP_NPOT_TEXTURES is equivalent to D3D_FEATURE_LEVEL_9_3

5.2.5 PIPE_TEXTURE_3D

3-dimensional array of texels. Mipmap dimensions are reduced in all 3 coordinates.

- **If PIPE_CAP_NPOT_TEXTURES is not supported**, width, height and depth must be powers of two
- Must use normalized coordinates

OpenGL: GL_TEXTURE_3D in GL 1.2 or GL_EXT_texture3D

- PIPE_CAP_NPOT_TEXTURES is equivalent to GL 2.0 or GL_ARB_texture_non_power_of_two

D3D11: 3D textures

- PIPE_CAP_NPOT_TEXTURES is equivalent to D3D_FEATURE_LEVEL_10_0

5.2.6 PIPE_TEXTURE_CUBE

Cube maps consist of 6 2D faces. The 6 surfaces form an imaginary cube, and sampling happens by mapping an input 3-vector to the point of the cube surface in that direction.

Sampling may be optionally seamless, resulting in filtering taking samples from multiple surfaces near to the edge.

UNIMPLEMENTED: seamless cube map sampling not supported

UNIMPLEMENTED: cube map arrays not supported

- Width and height must be equal
- **If PIPE_CAP_NPOT_TEXTURES is not supported**, width and height must be powers of two
- Must use normalized coordinates

OpenGL: GL_TEXTURE_CUBE_MAP in GL 1.3 or EXT_texture_cube_map

- PIPE_CAP_NPOT_TEXTURES is equivalent to GL 2.0 or GL_ARB_texture_non_power_of_two
- Seamless cube maps require GL 3.2 or GL_ARB_seamless_cube_map or GL_AMD_seamless_cubemap_per_texture

- Cube map arrays require GL 4.0 or GL_ARB_texture_cube_map_array

D3D11: 2D array textures with the D3D11_RESOURCE_MISC_TEXTURECUBE flag

- PIPE_CAP_NPOT_TEXTURES is equivalent to D3D_FEATURE_LEVEL_10_0
- Cube map arrays require D3D_FEATURE_LEVEL_10_1
- TODO: are (non)seamless cube maps supported in D3D11? how?

5.3 Surfaces

Surfaces are views of a resource that can be bound as a framebuffer to serve as the render target or depth buffer.

TODO: write much more on surfaces

OpenGL: FBOs are collections of surfaces in GL 3.0 or GL_ARB_framebuffer_object

D3D11: render target views and depth/stencil views

5.4 Sampler views

Sampler views are views of a resource that can be bound to a pipeline stage to be sampled from shaders.

TODO: write much more on sampler views

OpenGL: texture objects are actually sampler view and resource in a single unit

D3D11: shader resource views

A Gallium rendering context encapsulates the state which effects 3D rendering such as blend state, depth/stencil state, texture samplers, etc.

Note that resource/texture allocation is not per-context but per-screen.

6.1 Methods

6.1.1 CSO State

All Constant State Object (CSO) state is created, bound, and destroyed, with triplets of methods that all follow a specific naming scheme. For example, `create_blend_state`, `bind_blend_state`, and `destroy_blend_state`.

CSO objects handled by the context object:

- *Blend*: `*_blend_state`
- *Sampler*: Texture sampler states are bound separately for fragment, vertex and geometry samplers. Note that sampler states are set en masse. If M is the max number of sampler units supported by the driver and N samplers are bound with `bind_fragment_sampler_states` then sampler units N..M-1 are considered disabled/NULL.
- *Rasterizer*: `*_rasterizer_state`
- *Depth, Stencil, & Alpha*: `*_depth_stencil_alpha_state`
- *Shader*: These are create, bind and destroy methods for vertex, fragment and geometry shaders.
- *Vertex Elements*: `*_vertex_elements_state`

6.1.2 Resource Binding State

This state describes how resources in various flavours (textures, buffers, surfaces) are bound to the driver.

- `set_constant_buffer` sets a constant buffer to be used for a given shader type. `index` is used to indicate which buffer to set (some apis may allow multiple ones to be set, and binding a specific one later, though drivers are mostly restricted to the first one right now).
- `set_framebuffer_state`
- `set_vertex_buffers`

- `set_index_buffer`

6.1.3 Non-CSO State

These pieces of state are too small, variable, and/or trivial to have CSO objects. They all follow simple, one-method binding calls, e.g. `set_blend_color`.

- `set_stencil_ref` sets the stencil front and back reference values which are used as comparison values in stencil test.
- `set_blend_color`
- `set_sample_mask`
- `set_clip_state`
- `set_polygon_stipple`
- `set_scissor_state` sets the bounds for the scissor test, which culls pixels before blending to render targets. If the *Rasterizer* does not have the scissor test enabled, then the scissor bounds never need to be set since they will not be used. Note that scissor `xmin` and `ymin` are inclusive, but `xmax` and `ymax` are exclusive. The inclusive ranges in `x` and `y` would be `[xmin..xmax-1]` and `[ymin..ymax-1]`.
- `set_viewport_state`

6.1.4 Sampler Views

These are the means to bind textures to shader stages. To create one, specify its format, swizzle and LOD range in sampler view template.

If texture format is different than template format, it is said the texture is being cast to another format. Casting can be done only between compatible formats, that is formats that have matching component order and sizes.

Swizzle fields specify the way in which fetched texel components are placed in the result register. For example, `swizzle_r` specifies what is going to be placed in first component of result register.

The `first_level` and `last_level` fields of sampler view template specify the LOD range the texture is going to be constrained to. Note that these values are in addition to the respective `min_lod`, `max_lod` values in the `pipe_sampler_state` (that is if `min_lod` is 2.0, and `first_level` 3, the first mip level used for sampling from the resource is effectively the fifth).

The `first_layer` and `last_layer` fields specify the layer range the texture is going to be constrained to. Similar to the LOD range, this is added to the array index which is used for sampling.

- `set_fragment_sampler_views` binds an array of sampler views to fragment shader stage. Every binding point acquires a reference to a respective sampler view and releases a reference to the previous sampler view. If `M` is the maximum number of sampler units and `N` units is passed to `set_fragment_sampler_views`, the driver should unbind the sampler views for units `N..M-1`.
- `set_vertex_sampler_views` binds an array of sampler views to vertex shader stage. Every binding point acquires a reference to a respective sampler view and releases a reference to the previous sampler view.
- `create_sampler_view` creates a new sampler view. `texture` is associated with the sampler view which results in sampler view holding a reference to the texture. Format specified in template must be compatible with texture format.
- `sampler_view_destroy` destroys a sampler view and releases its reference to associated texture.

6.1.5 Shader Resources

Shader resources are textures or buffers that may be read or written from a shader without an associated sampler. This means that they have no support for floating point coordinates, address wrap modes or filtering.

Shader resources are specified for all the shader stages at once using the `set_shader_resources` method. When binding texture resources, the `level`, `first_layer` and `last_layer` `pipe_surface` fields specify the mipmap level and the range of layers the texture will be constrained to. In the case of buffers, `first_element` and `last_element` specify the range within the buffer that will be used by the shader resource. Writes to a shader resource are only allowed when the `writable` flag is set.

6.1.6 Surfaces

These are the means to use resources as color render targets or depthstencil attachments. To create one, specify the mip level, the range of layers, and the bind flags (either `PIPE_BIND_DEPTH_STENCIL` or `PIPE_BIND_RENDER_TARGET`). Note that layer values are in addition to what is indicated by the geometry shader output variable `XXX_FIXME` (that is if `first_layer` is 3 and geometry shader indicates index 2, the 5th layer of the resource will be used). These `first_layer` and `last_layer` parameters will only be used for 1d array, 2d array, cube, and 3d textures otherwise they are 0.

- `create_surface` creates a new surface.
- `surface_destroy` destroys a surface and releases its reference to the associated resource.

6.1.7 Stream output targets

Stream output, also known as transform feedback, allows writing the primitives produced by the vertex pipeline to buffers. This is done after the geometry shader or vertex shader if no geometry shader is present.

The stream output targets are views into buffer resources which can be bound as stream outputs and specify a memory range where it's valid to write primitives. The pipe driver must implement memory protection such that any primitives written outside of the specified memory range are discarded.

Two stream output targets can use the same resource at the same time, but with a disjoint memory range.

Additionally, the stream output target internally maintains the offset into the buffer which is incremented everytime something is written to it. The internal offset is equal to how much data has already been written. It can be stored in device memory and the CPU actually doesn't have to query it.

The stream output target can be used in a draw command to provide the vertex count. The vertex count is derived from the internal offset discussed above.

- `create_stream_output_target` create a new target.
- `stream_output_target_destroy` destroys a target. Users of this should use `pipe_so_target_reference` instead.
- `set_stream_output_targets` binds stream output targets. The parameter `append_bitmask` is a bitmask, where the *i*-th bit specifies whether new primitives should be appended to the *i*-th buffer (writing starts at the internal offset), or whether writing should start at the beginning (the internal offset is effectively set to 0).

NOTE: The currently-bound vertex or geometry shader must be compiled with the properly-filled-in structure `pipe_stream_output_info` describing which outputs should be written to buffers and how. The structure is part of `pipe_shader_state`.

6.1.8 Clearing

Clear is one of the most difficult concepts to nail down to a single interface (due to both different requirements from APIs and also driver/hw specific differences).

`clear` initializes some or all of the surfaces currently bound to the framebuffer to particular RGBA, depth, or stencil values. Currently, this does not take into account color or stencil write masks (as used by GL), and always clears the whole surfaces (no scissoring as used by GL clear or explicit rectangles like d3d9 uses). It can, however, also clear only depth or stencil in a combined depth/stencil surface, if the driver supports `PIPE_CAP_DEPTHSTENCIL_CLEAR_SEPARATE`. If a surface includes several layers then all layers will be cleared.

`clear_render_target` clears a single color rendertarget with the specified color value. While it is only possible to clear one surface at a time (which can include several layers), this surface need not be bound to the framebuffer.

`clear_depth_stencil` clears a single depth, stencil or depth/stencil surface with the specified depth and stencil values (for combined depth/stencil buffers, is is also possible to only clear one or the other part). While it is only possible to clear one surface at a time (which can include several layers), this surface need not be bound to the framebuffer.

6.1.9 Drawing

`draw_vbo` draws a specified primitive. The primitive mode and other properties are described by `pipe_draw_info`.

The `mode`, `start`, and `count` fields of `pipe_draw_info` specify the the mode of the primitive and the vertices to be fetched, in the range between `start` to `start`+`count-1`, inclusive.

Every instance with `instanceID` in the range between `start_instance` and `start_instance`+`instance_count-1`, inclusive, will be drawn.

If there is an index buffer bound, and `indexed` field is true, all vertex indices will be looked up in the index buffer.

In indexed draw, `min_index` and `max_index` respectively provide a lower and upper bound of the indices contained in the index buffer inside the range between `start` to `start`+`count-1`. This allows the driver to determine which subset of vertices will be referenced during te draw call without having to scan the index buffer. Providing a over-estimation of the the true bounds, for example, a `min_index` and `max_index` of 0 and 0xffffffff respectively, must give exactly the same rendering, albeit with less performance due to unreferenced vertex buffers being unnecessarily DMA'ed or processed. Providing a underestimation of the true bounds will result in undefined behavior, but should not result in program or system failure.

In case of non-indexed draw, `min_index` should be set to `start` and `max_index` should be set to `start`+`count-1`.

`index_bias` is a value added to every vertex index after lookup and before fetching vertex attributes.

When drawing indexed primitives, the primitive restart index can be used to draw disjoint primitive strips. For example, several separate line strips can be drawn by designating a special index value as the restart index. The `primitive_restart` flag enables/disables this feature. The `restart_index` field specifies the restart index value.

When primitive restart is in use, array indexes are compared to the restart index before adding the `index_bias` offset.

If a given vertex element has `instance_divisor` set to 0, it is said it contains per-vertex data and effective vertex attribute address needs to be recalculated for every index.

$$\text{attribAddr} = \text{stride} * \text{index} + \text{src_offset}$$

If a given vertex element has `instance_divisor` set to non-zero, it is said it contains per-instance data and effective vertex attribute address needs to be recalculated for every `instance_divisor`-th instance.

```
attribAddr = stride * instanceID / instance_divisor + src_offset
```

In the above formulas, `src_offset` is taken from the given vertex element and `stride` is taken from a vertex buffer associated with the given vertex element.

The calculated `attribAddr` is used as an offset into the vertex buffer to fetch the attribute data.

The value of `instanceID` can be read in a vertex shader through a system value register declared with `INSTANCEID` semantic name.

6.1.10 Queries

Queries gather some statistic from the 3D pipeline over one or more draws. Queries may be nested, though only `d3dx` currently exercises this.

Queries can be created with `create_query` and deleted with `destroy_query`. To start a query, use `begin_query`, and when finished, use `end_query` to end the query.

`get_query_result` is used to retrieve the results of a query. If the `wait` parameter is `TRUE`, then the `get_query_result` call will block until the results of the query are ready (and `TRUE` will be returned). Otherwise, if the `wait` parameter is `FALSE`, the call will not block and the return value will be `TRUE` if the query has completed or `FALSE` otherwise.

The interface currently includes the following types of queries:

`PIPE_QUERY_OCCLUSION_COUNTER` counts the number of fragments which are written to the framebuffer without being culled by Depth, Stencil, & Alpha testing or shader `KILL` instructions. The result is an unsigned 64-bit integer. This query can be used with `render_condition`.

In cases where a boolean result of an occlusion query is enough, `PIPE_QUERY_OCCLUSION_PREDICATE` should be used. It is just like `PIPE_QUERY_OCCLUSION_COUNTER` except that the result is a boolean value of `FALSE` for cases where `COUNTER` would result in 0 and `TRUE` for all other cases. This query can be used with `render_condition`.

`PIPE_QUERY_TIME_ELAPSED` returns the amount of time, in nanoseconds, the context takes to perform operations. The result is an unsigned 64-bit integer.

`PIPE_QUERY_TIMESTAMP` returns a device/driver internal timestamp, scaled to nanoseconds, recorded after all commands issued prior to `end_query` have been processed. This query does not require a call to `begin_query`. The result is an unsigned 64-bit integer.

`PIPE_QUERY_TIMESTAMP_DISJOINT` can be used to check whether the internal timer resolution is good enough to distinguish between the events at `begin_query` and `end_query`. The result is a 64-bit integer specifying the timer resolution in Hz, followed by a boolean value indicating whether the timer has incremented.

`PIPE_QUERY_PRIMITIVES_GENERATED` returns a 64-bit integer indicating the number of primitives processed by the pipeline.

`PIPE_QUERY_PRIMITIVES_EMITTED` returns a 64-bit integer indicating the number of primitives written to stream output buffers.

`PIPE_QUERY_SO_STATISTICS` returns 2 64-bit integers corresponding to the results of `PIPE_QUERY_PRIMITIVES_EMITTED` and `PIPE_QUERY_PRIMITIVES_GENERATED`, in this order.

`PIPE_QUERY_SO_OVERFLOW_PREDICATE` returns a boolean value indicating whether the stream output targets have overflowed as a result of the commands issued between `begin_query` and `end_query`. This query can be used with `render_condition`.

`PIPE_QUERY_GPU_FINISHED` returns a boolean value indicating whether all commands issued before `end_query` have completed. However, this does not imply serialization. This query does not require a call to `begin_query`.

`PIPE_QUERY_PIPELINE_STATISTICS` returns an array of the following 64-bit integers: Number of vertices read from vertex buffers. Number of primitives read from vertex buffers. Number of vertex shader threads launched. Number of geometry shader threads launched. Number of primitives generated by geometry shaders. Number of primitives forwarded to the rasterizer. Number of primitives rasterized. Number of fragment shader threads launched. Number of tessellation control shader threads launched. Number of tessellation evaluation shader threads launched. If a shader type is not supported by the device/driver, the corresponding values should be set to 0.

Gallium does not guarantee the availability of any query types; one must always check the capabilities of the *Screen* first.

6.1.11 Conditional Rendering

A drawing command can be skipped depending on the outcome of a query (typically an occlusion query). The `render_condition` function specifies the query which should be checked prior to rendering anything.

If `render_condition` is called with `query = NULL`, conditional rendering is disabled and drawing takes place normally.

If `render_condition` is called with a non-null `query` subsequent drawing commands will be predicated on the outcome of the query. If the query result is zero subsequent drawing commands will be skipped.

If `mode` is `PIPE_RENDER_COND_WAIT` the driver will wait for the query to complete before deciding whether to render.

If `mode` is `PIPE_RENDER_COND_NO_WAIT` and the query has not yet completed, the drawing command will be executed normally. If the query has completed, drawing will be predicated on the outcome of the query.

If `mode` is `PIPE_RENDER_COND_BY_REGION_WAIT` or `PIPE_RENDER_COND_BY_REGION_NO_WAIT` rendering will be predicated as above for the non-REGION modes but in the case that an occlusion query returns a non-zero result, regions which were occluded may be omitted by subsequent drawing commands. This can result in better performance with some GPUs. Normally, if the occlusion query returned a non-zero result subsequent drawing happens normally so fragments may be generated, shaded and processed even where they're known to be obscured.

6.1.12 Flushing

`flush`

6.1.13 Resource Busy Queries

`is_resource_referenced`

6.1.14 Blitting

These methods emulate classic blitter controls.

These methods operate directly on `pipe_resource` objects, and stand apart from any 3D state in the context. Blitting functionality may be moved to a separate abstraction at some point in the future.

`resource_copy_region` blits a region of a resource to a region of another resource, provided that both resources have the same format, or compatible formats, i.e., formats for which copying the bytes from the source resource unmodified to the destination resource will achieve the same effect of a textured quad blitter.. The source and destination may be the same resource, but overlapping blits are not permitted.

`resource_resolve` resolves a multisampled resource into a non-multisampled one. Their formats must match. This function must be present if a driver supports multisampling. The region that is to be resolved is described by `pipe_resolve_info`, which provides a source and a destination rectangle. The source rectangle may be vertically flipped, but otherwise the dimensions of the rectangles must match, unless `PIPE_CAP_SCALED_RESOLVE` is supported, in which case scaling and horizontal flipping are allowed as well. The result of resolving depth/stencil values may be any function of the values at the sample points, but returning the value of the centermost sample is preferred.

The interfaces to these calls are likely to change to make it easier for a driver to batch multiple blits with the same source and destination.

6.1.15 Transfers

These methods are used to get data to/from a resource.

`get_transfer` creates a transfer object.

`transfer_destroy` destroys the transfer object. May cause data to be written to the resource at this point.

`transfer_map` creates a memory mapping for the transfer object. The returned map points to the start of the mapped range according to the box region, not the beginning of the resource.

`transfer_unmap` remove the memory mapping for the transfer object. Any pointers into the map should be considered invalid and discarded.

`transfer_inline_write` performs a simplified transfer for simple writes. Basically `get_transfer`, `transfer_map`, data write, `transfer_unmap`, and `transfer_destroy` all in one.

The box parameter to some of these functions defines a 1D, 2D or 3D region of pixels. This is self-explanatory for 1D, 2D and 3D texture targets.

For `PIPE_TEXTURE_1D_ARRAY`, the `box::y` and `box::height` fields refer to the array dimension of the texture.

For `PIPE_TEXTURE_2D_ARRAY`, the `box::z` and `box::depth` fields refer to the array dimension of the texture.

For `PIPE_TEXTURE_CUBE`, the `box::z` and `box::depth` fields refer to the faces of the cube map ($z + \text{depth} \leq 6$).

transfer_flush_region

If a transfer was created with `FLUSH_EXPLICIT`, it will not automatically be flushed on write or unmap. Flushes must be requested with `transfer_flush_region`. Flush ranges are relative to the mapped range, not the beginning of the resource.

texture_barrier

This function flushes all pending writes to the currently-set surfaces and invalidates all read caches of the currently-set samplers.

6.1.16 PIPE_TRANSFER

These flags control the behavior of a transfer object.

PIPE_TRANSFER_READ Resource contents read back (or accessed directly) at transfer create time.

PIPE_TRANSFER_WRITE Resource contents will be written back at `transfer_destroy` time (or modified as a result of being accessed directly).

PIPE_TRANSFER_MAP_DIRECTLY a transfer should directly map the resource. May return NULL if not supported.

PIPE_TRANSFER_DISCARD_RANGE The memory within the mapped region is discarded. Cannot be used with `PIPE_TRANSFER_READ`.

PIPE_TRANSFER_DISCARD_WHOLE_RESOURCE Discards all memory backing the resource. It should not be used with `PIPE_TRANSFER_READ`.

PIPE_TRANSFER_DONTBLOCK Fail if the resource cannot be mapped immediately.

PIPE_TRANSFER_UNSYNCHRONIZED Do not synchronize pending operations on the resource when mapping. The interaction of any writes to the map and any operations pending on the resource are undefined. Cannot be used with `PIPE_TRANSFER_READ`.

PIPE_TRANSFER_FLUSH_EXPLICIT Written ranges will be notified later with *transfer_flush_region*. Cannot be used with `PIPE_TRANSFER_READ`.

6.1.17 Compute kernel execution

A compute program can be defined, bound or destroyed using `create_compute_state`, `bind_compute_state` or `destroy_compute_state` respectively.

Any of the subroutines contained within the compute program can be executed on the device using the `launch_grid` method. This method will execute as many instances of the program as elements in the specified N-dimensional grid, hopefully in parallel.

The compute program has access to four special resources:

- `GLOBAL` represents a memory space shared among all the threads running on the device. An arbitrary buffer created with the `PIPE_BIND_GLOBAL` flag can be mapped into it using the `set_global_binding` method.
- `LOCAL` represents a memory space shared among all the threads running in the same working group. The initial contents of this resource are undefined.
- `PRIVATE` represents a memory space local to a single thread. The initial contents of this resource are undefined.
- `INPUT` represents a read-only memory space that can be initialized at `launch_grid` time.

These resources use a byte-based addressing scheme, and they can be accessed from the compute program by means of the `LOAD/STORE` TGSI opcodes. Additional resources to be accessed using the same opcodes may be specified by the user with the `set_compute_resources` method.

In addition, normal texture sampling is allowed from the compute program: `bind_compute_sampler_states` may be used to set up texture samplers for the compute stage and `set_compute_sampler_views` may be used to bind a number of sampler views to it.

CSO, Constant State Objects, are a core part of Gallium's API.

CSO work on the principle of reusable state; they are created by filling out a state object with the desired properties, then passing that object to a context. The context returns an opaque context-specific handle which can be bound at any time for the desired effect.

7.1 Blend

This state controls blending of the final fragments into the target rendering buffers.

7.1.1 Blend Factors

The blend factors largely follow the same pattern as their counterparts in other modern and legacy drawing APIs.

Dual source blend factors are supported for up to 1 MRT, although you can advertise > 1 MRT, the stack cannot handle them for a few reasons. There is no definition on how the 1D array of shader outputs should be mapped to something that would be a 2D array (location, index). No current hardware exposes > 1 MRT, and we should revisit this issue if anyone ever does.

7.1.2 Logical Operations

Logical operations, also known as logicops, lops, or rops, are supported. Only two-operand logicops are available. When logicops are enabled, all other blend state is ignored, including per-render-target state, so logicops are performed on all render targets.

Warning: The `blend_enable` flag is ignored for all render targets when logical operations are enabled.

For a source component s and destination component d , the logical operations are defined as taking the bits of each channel of each component, and performing one of the following operations per-channel:

- CLEAR: 0
- NOR: $\neg(s \vee d)$
- AND_INVERTED: $\neg s \wedge d$
- COPY_INVERTED: $\neg s$
- AND_REVERSE: $s \wedge \neg d$

- INVERT: $\neg d$
- XOR: $s \oplus d$
- NAND: $\neg(s \wedge d)$
- AND: $s \wedge d$
- EQUIV: $\neg(s \oplus d)$
- NOOP: d
- OR_INVERTED: $\neg s \vee d$
- COPY: s
- OR_REVERSE: $s \vee \neg d$
- OR: $s \vee d$
- SET: 1

Note: The logical operation names and definitions match those of the OpenGL API, and are similar to the ROP2 and ROP3 definitions of GDI. This is intentional, to ease transitions to Gallium.

7.1.3 Members

These members affect all render targets.

dither

Whether dithering is enabled.

Note: Dithering is completely implementation-dependent. It may be ignored by drivers for any reason, and some render targets may always or never be dithered depending on their format or usage flags.

logicop_enable

Whether the blender should perform a logicop instead of blending.

logicop_func

The logicop to use. One of PIPE_LOGICOP.

independent_blend_enable If enabled, blend state is different for each render target, and for each render target set in the respective member of the rt array. If disabled, blend state is the same for all render targets, and only the first member of the rt array contains valid data.

rt Contains the per-rendertarget blend state.

7.1.4 Per-rendertarget Members

blend_enable If blending is enabled, perform a blend calculation according to blend functions and source/destination factors. Otherwise, the incoming fragment color gets passed unmodified (but colormask still applies).

rgb_func The blend function to use for rgb channels. One of PIPE_BLEND.

rgb_src_factor The blend source factor to use for rgb channels. One of PIPE_BLENDFACTOR.

rgb_dst_factor The blend destination factor to use for rgb channels. One of PIPE_BLENDFACTOR.

alpha_func The blend function to use for the alpha channel. One of PIPE_BLEND.

alpha_src_factor The blend source factor to use for the alpha channel. One of PIPE_BLENDFACTOR.

alpha_dst_factor The blend destination factor to use for alpha channel. One of PIPE_BLENDFACTOR.

colormask Bitmask of which channels to write. Combination of PIPE_MASK bits.

7.2 Depth, Stencil, & Alpha

These three states control the depth, stencil, and alpha tests, used to discard fragments that have passed through the fragment shader.

Traditionally, these three tests have been clumped together in hardware, so they are all stored in one structure.

During actual execution, the order of operations done on fragments is always:

- Alpha
- Stencil
- Depth

7.2.1 Depth Members

enabled Whether the depth test is enabled.

writemask Whether the depth buffer receives depth writes.

func The depth test function. One of PIPE_FUNC.

7.2.2 Stencil Members

enabled Whether the stencil test is enabled. For the second stencil, whether the two-sided stencil is enabled. If two-sided stencil is disabled, the other fields for the second array member are not valid.

func The stencil test function. One of PIPE_FUNC.

valuemask Stencil test value mask; this is ANDed with the value in the stencil buffer and the reference value before doing the stencil comparison test.

writemask Stencil test writemask; this controls which bits of the stencil buffer are written.

fail_op The operation to carry out if the stencil test fails. One of PIPE_STENCIL_OP.

zfail_op The operation to carry out if the stencil test passes but the depth test fails. One of PIPE_STENCIL_OP.

zpass_op The operation to carry out if the stencil test and depth test both pass. One of PIPE_STENCIL_OP.

7.2.3 Alpha Members

enabled Whether the alpha test is enabled.

func The alpha test function. One of PIPE_FUNC.

ref_value Alpha test reference value; used for certain functions.

7.3 Rasterizer

The rasterizer state controls the rendering of points, lines and triangles. Attributes include polygon culling state, line width, line stipple, multisample state, scissoring and flat/smooth shading.

Linkage

7.3.1 clamp_vertex_color

If set, TGSI_SEMANTIC_COLOR registers are clamped to the [0, 1] range after the execution of the vertex shader, before being passed to the geometry shader or fragment shader.

OpenGL: glClampColor(GL_CLAMP_VERTEX_COLOR) in GL 3.0 or GL_ARB_color_buffer_float

D3D11: seems always disabled

Note the PIPE_CAP_VERTEX_COLOR_CLAMPED query indicates whether or not the driver supports this control. If it's not supported, the state tracker may have to insert extra clamping code.

7.3.2 clamp_fragment_color

Controls whether TGSI_SEMANTIC_COLOR outputs of the fragment shader are clamped to [0, 1].

OpenGL: glClampColor(GL_CLAMP_FRAGMENT_COLOR) in GL 3.0 or ARB_color_buffer_float

D3D11: seems always disabled

Note the PIPE_CAP_FRAGMENT_COLOR_CLAMPED query indicates whether or not the driver supports this control. If it's not supported, the state tracker may have to insert extra clamping code.

Shading

7.3.3 flatshade

If set, the provoking vertex of each polygon is used to determine the color of the entire polygon. If not set, fragment colors will be interpolated between the vertex colors.

The actual interpolated shading algorithm is obviously implementation-dependent, but will usually be Gourard for most hardware.

Note: This is separate from the fragment shader input attributes CONSTANT, LINEAR and PERSPECTIVE. The flatshade state is needed at clipping time to determine how to set the color of new vertices.

Draw can implement flat shading by copying the provoking vertex color to all the other vertices in the primitive.

7.3.4 flatshade_first

Whether the first vertex should be the provoking vertex, for most primitives. If not set, the last vertex is the provoking vertex.

There are a few important exceptions to the specification of this rule.

- `PIPE_PRIMITIVE_POLYGON`: The provoking vertex is always the first vertex. If the caller wishes to change the provoking vertex, they merely need to rotate the vertices themselves.
- `PIPE_PRIMITIVE_QUAD`, `PIPE_PRIMITIVE_QUAD_STRIP`: The option only has an effect if `PIPE_CAP_QUADS_FOLLOW_PROVOKING_VERTEX_CONVENTION` is true. If it is not, the provoking vertex is always the last vertex.
- `PIPE_PRIMITIVE_TRIANGLE_FAN`: When set, the provoking vertex is the second vertex, not the first. This permits each segment of the fan to have a different color.

Polygons

7.3.5 light_twoside

If set, there are per-vertex back-facing colors. The hardware (perhaps assisted by *Draw*) should be set up to use this state along with the front/back information to set the final vertex colors prior to rasterization.

The frontface vertex shader color output is marked with TGSI semantic `COLOR[0]`, and backface `COLOR[1]`.

front_ccw Indicates whether the window order of front-facing polygons is counter-clockwise (TRUE) or clockwise (FALSE).

cull_mode Indicates which faces of polygons to cull, either `PIPE_FACE_NONE` (cull no polygons), `PIPE_FACE_FRONT` (cull front-facing polygons), `PIPE_FACE_BACK` (cull back-facing polygons), or `PIPE_FACE_FRONT_AND_BACK` (cull all polygons).

fill_front Indicates how to fill front-facing polygons, either `PIPE_POLYGON_MODE_FILL`, `PIPE_POLYGON_MODE_LINE` or `PIPE_POLYGON_MODE_POINT`.

fill_back Indicates how to fill back-facing polygons, either `PIPE_POLYGON_MODE_FILL`, `PIPE_POLYGON_MODE_LINE` or `PIPE_POLYGON_MODE_POINT`.

poly_stipple_enable Whether polygon stippling is enabled.

poly_smooth Controls OpenGL-style polygon smoothing/antialiasing

offset_point If set, point-filled polygons will have polygon offset factors applied

offset_line If set, line-filled polygons will have polygon offset factors applied

offset_tri If set, filled polygons will have polygon offset factors applied

offset_units Specifies the polygon offset bias

offset_scale Specifies the polygon offset scale

offset_clamp Upper (if > 0) or lower (if < 0) bound on the polygon offset result

Lines

line_width The width of lines.

line_smooth Whether lines should be smoothed. Line smoothing is simply anti-aliasing.

line_stipple_enable Whether line stippling is enabled.

line_stipple_pattern 16-bit bitfield of on/off flags, used to pattern the line stipple.

line_stipple_factor When drawing a stippled line, each bit in the stipple pattern is repeated N times, where $N = \text{line_stipple_factor} + 1$.

line_last_pixel Controls whether the last pixel in a line is drawn or not. OpenGL omits the last pixel to avoid double-drawing pixels at the ends of lines when drawing connected lines.

Points

7.3.6 `sprite_coord_enable`

Controls automatic texture coordinate generation for rendering sprite points.

When bit *k* in the `sprite_coord_enable` bitfield is set, then generic input *k* to the fragment shader will get an automatically computed texture coordinate.

The texture coordinate will be of the form (s, t, 0, 1) where *s* varies from 0 to 1 from left to right while *t* varies from 0 to 1 according to the state of 'sprite_coord_mode' (see below).

If any bit is set, then `point_smooth` MUST be disabled (there are no round sprites) and `point_quad_rasterization` MUST be true (sprites are always rasterized as quads). Any mismatch between these states should be considered a bug in the state-tracker.

This feature is implemented in the *Draw* module but may also be implemented natively by GPUs or implemented with a geometry shader.

7.3.7 `sprite_coord_mode`

Specifies how the value for each shader output should be computed when drawing point sprites. For `PIPE_SPRITE_COORD_LOWER_LEFT`, the lower-left vertex will have coordinates (0,0,0,1). For `PIPE_SPRITE_COORD_UPPER_LEFT`, the upper-left vertex will have coordinates (0,0,0,1). This state is used by *Draw* to generate texcoords.

7.3.8 `point_quad_rasterization`

Determines if points should be rasterized according to quad or point rasterization rules.

OpenGL actually has quite different rasterization rules for points and point sprites - hence this indicates if points should be rasterized as points or according to point sprite (which decomposes them into quads, basically) rules.

Additionally Direct3D will always use quad rasterization rules for points, regardless of whether point sprites are enabled or not.

If this state is enabled, point smoothing and antialiasing are disabled. If it is disabled, point sprite coordinates are not generated.

Note: Some renderers always internally translate points into quads; this state still affects those renderers by overriding other rasterization state.

point_smooth Whether points should be smoothed. Point smoothing turns rectangular points into circles or ovals.

point_size_per_vertex Whether the vertex shader is expected to have a point size output. Undefined behaviour is permitted if there is disagreement between this flag and the actual bound shader.

point_size The size of points, if not specified per-vertex.

Other Members

scissor Whether the scissor test is enabled.

multisample Whether *MSAA* is enabled.

gl_rasterization_rules Whether the rasterizer should use (0.5, 0.5) pixel centers. When not set, the rasterizer will use (0, 0) for pixel centers.

depth_clip When false, the near and far depth clipping planes of the view volume are disabled and the depth value will be clamped at the per-pixel level, after polygon offset has been applied and before depth testing.

clip_plane_enable For each k in $[0, \text{PIPE_MAX_CLIP_PLANES})$, if bit k of this field is set, clipping half-space k is enabled, if it is clear, it is disabled. The clipping half-spaces are defined either by the user clip planes in `pipe_clip_state`, or by the clip distance outputs of the shader stage preceding the fragment shader. If any clip distance output is written, those half-spaces for which no clip distance is written count as disabled; i.e. user clip planes and shader clip distances cannot be mixed, and clip distances take precedence.

7.4 Sampler

Texture units have many options for selecting texels from loaded textures; this state controls an individual texture unit's texel-sampling settings.

Texture coordinates are always treated as four-dimensional, and referred to with the traditional (S, T, R, Q) notation.

7.4.1 Members

wrap_s How to wrap the S coordinate. One of `PIPE_TEX_WRAP_*`.

wrap_t How to wrap the T coordinate. One of `PIPE_TEX_WRAP_*`.

wrap_r How to wrap the R coordinate. One of `PIPE_TEX_WRAP_*`.

The wrap modes are:

- `PIPE_TEX_WRAP_REPEAT`: Standard coord repeat/wrap-around mode.
- `PIPE_TEX_WRAP_CLAMP_TO_EDGE`: Clamp coord to edge of texture, the border color is never sampled.
- `PIPE_TEX_WRAP_CLAMP_TO_BORDER`: Clamp coord to border of texture, the border color is sampled when coords go outside the range $[0,1]$.
- `PIPE_TEX_WRAP_CLAMP`: The coord is clamped to the range $[0,1]$ before scaling to the texture size. This corresponds to the legacy OpenGL `GL_CLAMP` texture wrap mode. Historically, this mode hasn't acted consistently across all graphics hardware. It sometimes acts like `CLAMP_TO_EDGE` or `CLAMP_TO_BORDER`. The behaviour may also vary depending on linear vs. nearest sampling mode.
- `PIPE_TEX_WRAP_MIRROR_REPEAT`: If the integer part of the coordinate is odd, the coord becomes (1 - coord). Then, normal texture `REPEAT` is applied to the coord.
- `PIPE_TEX_WRAP_MIRROR_CLAMP_TO_EDGE`: First, the absolute value of the coordinate is computed. Then, regular `CLAMP_TO_EDGE` is applied to the coord.
- `PIPE_TEX_WRAP_MIRROR_CLAMP_TO_BORDER`: First, the absolute value of the coordinate is computed. Then, regular `CLAMP_TO_BORDER` is applied to the coord.
- `PIPE_TEX_WRAP_MIRROR_CLAMP`: First, the absolute value of the coord is computed. Then, regular `CLAMP` is applied to the coord.

min_img_filter The image filter to use when minifying texels. One of `PIPE_TEX_FILTER_*`.

mag_img_filter The image filter to use when magnifying texels. One of PIPE_TEX_FILTER_*

The texture image filter modes are:

- PIPE_TEX_FILTER_NEAREST: One texel is fetched from the texture image at the texture coordinate.
- PIPE_TEX_FILTER_LINEAR: Two, four or eight texels (depending on the texture dimensions; 1D/2D/3D) are fetched from the texture image and linearly weighted and blended together.

min_mip_filter The filter to use when minifying mipmapped textures. One of PIPE_TEX_MIPFILTER_*

The texture mip filter modes are:

- PIPE_TEX_MIPFILTER_NEAREST: A single mipmap level/image is selected according to the texture LOD (lambda) value.
- PIPE_TEX_MIPFILTER_LINEAR: The two mipmap levels/images above/below the texture LOD value are sampled from. The results of sampling from those two images are blended together with linear interpolation.
- PIPE_TEX_MIPFILTER_NONE: Mipmap filtering is disabled. All texels are taken from the level 0 image.

compare_mode If set to PIPE_TEX_COMPARE_R_TO_TEXTURE, the result of texture sampling is not a color but a true/false value which is the result of comparing the sampled texture value (typically a Z value from a depth texture) to the texture coordinate's R component. If set to PIPE_TEX_COMPARE_NONE, no comparison calculation is performed.

compare_func The inequality operator used when compare_mode=1. One of PIPE_FUNC_x.

normalized_coords If set, the incoming texture coordinates (nominally in the range [0,1]) will be scaled by the texture width, height, depth to compute texel addresses. Otherwise, the texture coords are used as-is (they are not scaled by the texture dimensions). When normalized_coords=0, only a subset of the texture wrap modes are allowed: PIPE_TEX_WRAP_CLAMP, PIPE_TEX_WRAP_CLAMP_TO_EDGE and PIPE_TEX_WRAP_CLAMP_TO_BORDER.

lod_bias Bias factor which is added to the computed level of detail. The normal level of detail is computed from the partial derivatives of the texture coordinates and/or the fragment shader TEX/TXB/TXL instruction.

min_lod Minimum level of detail, used to clamp LOD after bias. The LOD values correspond to mipmap levels where LOD=0 is the level 0 mipmap image.

max_lod Maximum level of detail, used to clamp LOD after bias.

border_color Color union used for texel coordinates that are outside the [0,width-1], [0, height-1] or [0, depth-1] ranges. Interpreted according to sampler view format.

max_anisotropy Maximum anisotropy ratio to use when sampling from textures. For example, if max_anisotropy=4, a region of up to 1 by 4 texels will be sampled. Set to zero to disable anisotropic filtering. Any other setting enables anisotropic filtering, however it's not unexpected some drivers only will change their filtering with a setting of 2 and higher.

seamless_cube_map If set, the bilinear filter of a cube map may take samples from adjacent cube map faces when sampled near a texture border to produce a seamless look.

7.5 Shader

One of the two types of shaders supported by Gallium.

7.5.1 Members

tokens A list of tgsi_tokens.

7.6 Vertex Elements

This state controls the format of the input attributes contained in `pipe_vertex_buffers`. There is one `pipe_vertex_element` array member for each input attribute.

7.6.1 Input Formats

Gallium supports a diverse range of formats for vertex data. Drivers are guaranteed to support 32-bit floating-point vectors of one to four components. Additionally, they may support the following formats:

- Integers, signed or unsigned, normalized or non-normalized, 8, 16, or 32 bits wide
- Floating-point, 16, 32, or 64 bits wide

At this time, support for varied vertex data formats is limited by driver deficiencies. It is planned to support a single uniform set of formats for all Gallium drivers at some point.

Rather than attempt to specify every small nuance of behavior, Gallium uses a very simple set of rules for padding out unspecified components. If an input uses less than four components, it will be padded out with the constant vector $(0, 0, 0, 1)$.

Fog, point size, the facing bit, and edgeflags, all are in the standard format of $(x, 0, 0, 1)$, and so only the first component of those inputs is used.

Position

Vertex position may be specified with two to four components. Using less than two components is not allowed.

Colors

Colors, both front- and back-facing, may omit the alpha component, only using three components. Using less than three components is not allowed.

7.6.2 Members

src_offset The byte offset of the attribute in the buffer given by `vertex_buffer_index` for the first vertex.

instance_divisor The instance data rate divisor, used for instancing. 0 means this is per-vertex data, n means per-instance data used for n consecutive instances ($n > 0$).

vertex_buffer_index The vertex buffer this attribute lives in. Several attributes may live in the same vertex buffer.

src_format The format of the attribute data. One of the `PIPE_FORMAT` tokens.

Distribution

Along with the interface definitions, the following drivers, state trackers, and auxiliary modules are shipped in the standard Gallium distribution.

8.1 Drivers

8.1.1 Intel i915

Driver for Intel i915 and i945 chipsets.

8.1.2 Identity

Wrapper driver. The identity driver is a simple skeleton that passes through all of its *Context* and *Screen* methods to an underlying Context and Screen, and as such, it is an excellent starting point for new drivers.

8.1.3 LLVM Softpipe

A version of *Softpipe* that uses the Low-Level Virtual Machine to dynamically generate optimized rasterizing pipelines.

8.1.4 nVidia nvfx

Driver for the nVidia nv30 and nv40 families of GPUs.

8.1.5 nVidia nv50

Driver for the nVidia nv50 family of GPUs.

8.1.6 VMware SVGA

Driver for VMware virtualized guest operating system graphics processing.

8.1.7 ATI r300

Driver for the ATI/AMD r300, r400, and r500 families of GPUs.

8.1.8 Softpipe

Reference software rasterizer. Slow but accurate.

8.1.9 Trace

Wrapper driver. Trace dumps an XML record of the calls made to the *Context* and *Screen* objects that it wraps.

8.1.10 Rbug

Wrapper driver. *Remote Debugger* driver used with stand alone rbug-gui.

8.1.11 Galahad

Wrapper driver. Sanity checker for the internal gallium state. Normally a driver should n't have to sanity check the input it gets from a state tracker. Any wrong state received should be perceived as a state tracker bug.

8.2 State Trackers

8.2.1 Direct Rendering Infrastructure

Tracker that implements the client-side DRI protocol, for providing direct acceleration services to X11 servers with the DRI extension. Supports DRI1 and DRI2. Only GL is supported.

8.2.2 EGL

Tracker for the Khronos EGL standard, used to set up GL and GLES contexts without extra knowledge of the underlying windowing system.

8.2.3 GLX

8.2.4 MesaGL

Tracker implementing a GL state machine. Not usable as a standalone tracker; Mesa should be built with another state tracker, such as *Direct Rendering Infrastructure* or *EGL*.

8.2.5 Python

8.2.6 OpenVG

8.2.7 WGL

8.2.8 Xorg/XFree86 DDX

Tracker for XFree86 and Xorg X11 servers. Provides device-dependent modesetting and acceleration as a DDX driver.

8.3 Auxiliary

8.3.1 OS

The OS module contains the abstractions for basic operating system services:

- memory allocation
- simple message logging
- obtaining run-time configuration option
- threading primitives

This is the bare minimum required to port Gallium to a new platform.

The OS module already provides the implementations of these abstractions for the most common platforms. When targeting an embedded platform no implementation will be provided – these must be provided separately.

8.3.2 CSO Cache

The CSO cache is used to accelerate preparation of state by saving driver-specific state structures for later use.

8.3.3 Draw

Draw is a software *TCL* pipeline for hardware that lacks vertex shaders or other essential parts of pre-rasterization vertex preparation.

8.3.4 Gallivm

8.3.5 Indices

Indices provides tools for translating or generating element indices for use with element-based rendering.

8.3.6 Pipe Buffer Managers

Each of these managers provides various services to drivers that are not fully utilizing a memory manager.

8.3.7 Remote Debugger

8.3.8 Runtime Assembly Emission

8.3.9 TGSi

The TGSi auxiliary module provides basic utilities for manipulating TGSi streams.

8.3.10 Translate

8.3.11 Util

Glossary

GLSL GL Shading Language. The official, common high-level shader language used in GL 2.0 and above.

layer This term is used as the name of the “3rd coordinate” of a resource. 3D textures have z-slices, cube maps have faces, 1D and 2D array textures have array members (other resources do not have multiple layers). Since the functions only take one parameter no matter what type of resource is used, use the term “layer” instead of a resource type specific one.

LOD Level of Detail. Also spelled “LoD.” The value that determines when the switches between mipmaps occur during texture sampling.

MSAA Multi-Sampled Anti-Aliasing. A basic anti-aliasing technique that takes multiple samples of the depth buffer, and uses this information to smooth the edges of polygons.

NPOT Non-power-of-two. Usually applied to textures which have at least one dimension which is not a power of two.

TCL Transform, Clipping, & Lighting. The three stages of preparation in a rasterizing pipeline prior to the actual rasterization of vertices into fragments.

Indices and tables

- `genindex`
- `modindex`
- `search`

Symbols

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